Bit/Reg Address	Modbus Protocol Address		Number Of Registers	Name	Format	Default	Range	Units or Resolution	Access	Description
	Dec	Hex								
Input Section										
Discrete Outputs										
101025	1024	0x0400	N/A	Proportional Output #1	Bit	N/A	0…1	State	RO	
101026	1025	0x0401	N/A	Proportional Output #2	Bit	N/A	0…1	State	RO	
101027	1026	0x0402	N/A	Proportional Output #3	Bit	N/A	0…1	State	RO	
101028	1027	0x0403	N/A	Proportional Output #4	Bit	N/A	0…1	State	RO	
Discrete Outputs										
301025	1024	0x0400	1	Discrete Outputs	Bits	N/A	0…0x3ff	1 bit per output	RO	Bit 0 (LSB) - Universal Output #1, Bit 1 -Universal Output #2, ... Bit 3 - Universal Output #4. When output is not in "Discrete Voltage Level" mode, the output state is 0.
Proportional Outputs										
301026	1025	0x0401	2	Proportional Output #1	Float	N/A	See output config	See config	RO	
301028	1027	0x0403	2	Proportional Output #2	Float	N/A	See output config	See config	RO	
301030	1029	0x0405	2	Proportional Output #3	Float	N/A	See output config	See config	RO	
301032	1031	0x0407	2	Proportional Output #4	Float	N/A	See output config	See config	RO	
301032	1033	0x0407	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Thermocouple Inputs										
301042	1041	0x0411	2	Thermocouple Input #1	Float	N/A	See output config	See config	RO	
301044	1043	0x0413	2	Thermocouple Input #2	Float	N/A	See output config	See config	RO	

301046	1045	0x0415	2	Thermocouple Input #3	Float	N/A	See output config	See config	RO	
301048	1047	0x0417	2	Thermocouple Input #4	Float	N/A	See output config	See config	RO	
301050	1049	0x0419	2	Thermocouple Input #5	Float	N/A	See output config	See config	RO	
301052	1051	0x041B	2	Thermocouple Input #6	Float	N/A	See output config	See config	RO	
301054	1053	0x041D	2	Thermocouple Input #7	Float	N/A	See output config	See config	RO	
301056	1055	0x041F	2	Thermocouple Input #8	Float	N/A	See output config	See config	RO	
301058	1057	0x0421	2	Thermocouple Input #9	Float	N/A	See output config	See config	RO	
301060	1059	0x0423	2	Thermocouple Input #10	Float	N/A	See output config	See config	RO	
301062	1061	0x0425	2	Thermocouple Input #11	Float	N/A	See output config	See config	RO	
301064	1063	0x0427	2	Thermocouple Input #12	Float	N/A	See output config	See config	RO	
301066	1065	0x0429	2	Thermocouple Input #13	Float	N/A	See output config	See config	RO	
301068	1067	0x042B	2	Thermocouple Input #14	Float	N/A	See output config	See config	RO	
301070	1069	0x042D	2	Thermocouple Input #15	Float	N/A	See output config	See config	RO	
301072	1071	0x042F	2	Thermocouple Input #16	Float	N/A	See output config	See config	RO	
301074	1073	0x0431	2	Thermocouple Input #17	Float	N/A	See output config	See config	RO	
301076	1075	0x0433	2	Thermocouple Input #18	Float	N/A	See output config	See config	RO	
301078	1077	0x0435	2	Thermocouple Input #19	Float	N/A	See output config	See config	RO	
301080	1079	0x0437	2	Thermocouple Input #20	Float	N/A	See output config	See config	RO	
301082	1081	0x0439	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Thermocouple mV Readings										
301090	1089	0x0441	2	Thermocouple Input[mV] #1	Float	N/A	See output config	See config	RO	
301092	1091	0x0443	2	Thermocouple Input[mV] #2	Float	N/A	See output config	See config	RO	
301094	1093	0x0445	2	Thermocouple Input[mV] #3	Float	N/A	See output config	See config	RO	
301096	1095	0x0447	2	Thermocouple Input[mV] #4	Float	N/A	See output config	See config	RO	
301098	1097	0x0449	2	Thermocouple Input[mV] #5	Float	N/A	See output config	See config	RO	
301100	1099	0x044B	2	Thermocouple Input[mV] #6	Float	N/A	See output config	See config	RO	
301102	1101	0x044D	2	Thermocouple Input[mV] #7	Float	N/A	See output config	See config	RO	
301104	1103	0x044F	2	Thermocouple Input[mV] #8	Float	N/A	See output config	See config	RO	

30111 06	1105	0x0451	2	Thermocouple Input[mV] #9	Float	N/A	See output config	See config	RO	
30111 08	1107	0x0453	2	Thermocouple Input[mV] #10	Float	N/A	See output config	See config	RO	
30111 10	1109	0x0455	2	Thermocouple Input[mV] #11	Float	N/A	See output config	See config	RO	
30111 12	1111	0x0457	2	Thermocouple Input[mV] #12	Float	N/A	See output config	See config	RO	
30111 14	1113	0x0459	2	Thermocouple Input[mV] #13	Float	N/A	See output config	See config	RO	
30111 16	1115	0x045B	2	Thermocouple Input[mV] #14	Float	N/A	See output config	See config	RO	
30111 18	1117	0x045D	2	Thermocouple Input[mV] #15	Float	N/A	See output config	See config	RO	
30111 20	1119	0x045F	2	Thermocouple Input[mV] #16	Float	N/A	See output config	See config	RO	
30111 22	1121	0x0461	2	Thermocouple Input[mV] #17	Float	N/A	See output config	See config	RO	
30111 24	1123	0x0463	2	Thermocouple Input[mV] #18	Float	N/A	See output config	See config	RO	
30111 26	1125	0x0465	2	Thermocouple Input[mV] #19	Float	N/A	See output config	See config	RO	
30111 28	1127	0x0467	2	Thermocouple Input[mV] #20	Float	N/A	See output config	See config	RO	
30111 30	1129	0x0469	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Thermocouple Raw Readings										
30111 38	1137	0x0471	2	Thermocouple Raw Input #1	Float	N/A	See output config	See config	RO	
30111 40	1139	0x0473	2	Thermocouple Raw Input #2	Float	N/A	See output config	See config	RO	
30111 42	1141	0x0475	2	Thermocouple Raw Input #3	Float	N/A	See output config	See config	RO	
30111 44	1143	0x0477	2	Thermocouple Raw Input #4	Float	N/A	See output config	See config	RO	
30111 46	1145	0x0479	2	Thermocouple Raw Input #5	Float	N/A	See output config	See config	RO	
30111 48	1147	0x047B	2	Thermocouple Raw Input #6	Float	N/A	See output config	See config	RO	
30111 50	1149	0x047D	2	Thermocouple Raw Input #7	Float	N/A	See output config	See config	RO	
30111 52	1151	0x047F	2	Thermocouple Raw Input #8	Float	N/A	See output config	See config	RO	
30111 54	1153	0x0481	2	Thermocouple Raw Input #9	Float	N/A	See output config	See config	RO	
30111 56	1155	0x0483	2	Thermocouple Raw Input #10	Float	N/A	See output config	See config	RO	
30111 58	1157	0x0485	2	Thermocouple Raw Input #11	Float	N/A	See output config	See config	RO	
30111 60	1159	0x0487	2	Thermocouple Raw Input #12	Float	N/A	See output config	See config	RO	
30111 62	1161	0x0489	2	Thermocouple Raw Input #13	Float	N/A	See output config	See config	RO	
30111 64	1163	0x048B	2	Thermocouple Raw Input #14	Float	N/A	See output config	See config	RO	

30116	1165	0x048D	2	Thermocouple Raw Input #15	Float	N/A	See output config	See config	RO	
30116	1167	0x048F	2	Thermocouple Raw Input #16	Float	N/A	See output config	See config	RO	
30117	1169	0x0491	2	Thermocouple Raw Input #17	Float	N/A	See output config	See config	RO	
30117	1171	0x0493	2	Thermocouple Raw Input #18	Float	N/A	See output config	See config	RO	
30117	1173	0x0495	2	Thermocouple Raw Input #19	Float	N/A	See output config	See config	RO	
30117	1175	0x0497	2	Thermocouple Raw Input #20	Float	N/A	See output config	See config	RO	
30117	1177	0x0499	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
RTD Input										
30118	1185	0x04A1	2	RTD Input #1	Float	N/A	See output config	See config	RO	
30118	1187	0x04A3	2	RTD Input #2	Float	N/A	See output config	See config	RO	
30119	1189	0x04A5	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
RTD Resistance Readings										
30120	1201	0x04B1	2	RTD Resistance #1	Float	N/A	See output config	See config	RO	
30120	1203	0x04B3	2	RTD Resistance #2	Float	N/A	See output config	See config	RO	
30120	1205	0x04B5	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
RTD Raw Readings										
30121	1217	0x04C1	2	RTD Raw Readings #1	Float	N/A	See output config	See config	RO	
30121	1219	0x04C3	2	RTD Raw Readings #2	Float	N/A	See output config	See config	RO	
30122	1221	0x04C5	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Math Output										
30123	1233	0x04D1	2	Math Output #1	Float	N/A	See output config	See config	RO	
30123	1235	0x04D3	2	Math Output #2	Float	N/A	See output config	See config	RO	
30123	1237	0x04D5	2	Math Output #3	Float	N/A	See output config	See config	RO	
30124	1239	0x04D7	2	Math Output #4	Float	N/A	See output config	See config	RO	
30124	1241	0x04D9	2	Math Output #5	Float	N/A	See output config	See config	RO	
30124	1243	0x04DB	6	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0

Conditional Logic Output										
30125	1249	0x04E1	2	Conditional Logic Output #1	Float	N/A	See output config	See config	RO	
30127	1251	0x04E3	2	Conditional Logic Output #2	Float	N/A	See output config	See config	RO	
30129	1253	0x04E5	2	Conditional Logic Output #3	Float	N/A	See output config	See config	RO	
30131	1255	0x04E7	2	Conditional Logic Output #4	Float	N/A	See output config	See config	RO	
30133	1257	0x04E9	2	Conditional Logic Output #5	Float	N/A	See output config	See config	RO	
30135	1259	0x04EB	2	Conditional Logic Output #6	Float	N/A	See output config	See config	RO	
30137	1261	0x04ED	2	Conditional Logic Output #7	Float	N/A	See output config	See config	RO	
30139	1263	0x04EF	2	Conditional Logic Output #8	Float	N/A	See output config	See config	RO	
30141	1265	0x04F1	2	Conditional Logic Output #9	Float	N/A	See output config	See config	RO	
30143	1267	0x04F3	2	Conditional Logic Output #10	Float	N/A	See output config	See config	RO	
30145	1269	0x04F5	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Set-Reset Latch Output										
30181	1281	0x0501	2	Set-Reset Latch Output #1	Float	N/A	See output config	See config	RO	
30183	1283	0x0503	2	Set-Reset Latch Output #2	Float	N/A	See output config	See config	RO	
30185	1285	0x0505	2	Set-Reset Latch Output #3	Float	N/A	See output config	See config	RO	
30187	1287	0x0507	2	Set-Reset Latch Output #4	Float	N/A	See output config	See config	RO	
30189	1289	0x0509	2	Set-Reset Latch Output #5	Float	N/A	See output config	See config	RO	
30191	1291	0x050B	6	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Lookup Table Output										
30197	1297	0x0511	2	Lookup Table Output #1	Float	N/A	See output config	See config	RO	
30199	1299	0x0513	2	Lookup Table Output #2	Float	N/A	See output config	See config	RO	
30201	1301	0x0515	2	Lookup Table Output #3	Float	N/A	See output config	See config	RO	
30203	1303	0x0517	2	Lookup Table Output #4	Float	N/A	See output config	See config	RO	
30205	1305	0x0519	2	Lookup Table Output #5	Float	N/A	See output config	See config	RO	
30207	1307	0x051B	2	Lookup Table Output #6	Float	N/A	See output config	See config	RO	

30131	1309	0x051D	2	Lookup Table Output #7	Float	N/A	See output config	See config	RO	
30133	1311	0x051F	2	Lookup Table Output #8	Float	N/A	See output config	See config	RO	
30135	1313	0x0521	2	Lookup Table Output #9	Float	N/A	See output config	See config	RO	
30137	1315	0x0523	2	Lookup Table Output #10	Float	N/A	See output config	See config	RO	
30139	1317	0x0525	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Programmable Logic Output										
30161	1329	0x0531	2	Programmable Logic Output #1	Float	N/A	See output config	See config	RO	
30163	1331	0x0533	2	Programmable Logic Output #2	Float	N/A	See output config	See config	RO	
30165	1333	0x0535	2	Programmable Logic Output #3	Float	N/A	See output config	See config	RO	
30167	1335	0x0537	10	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
CAN Inputs										
30191	1345	0x0541	2	CAN Receive Signal #1	Float	N/A	See input config	See config	RO	
30193	1347	0x0543	2	CAN Receive Signal #2	Float	N/A	See input config	See config	RO	
30195	1349	0x0545	2	CAN Receive Signal #3	Float	N/A	See input config	See config	RO	
30197	1351	0x0547	2	CAN Receive Signal #4	Float	N/A	See input config	See config	RO	
30199	1353	0x0549	2	CAN Receive Signal #5	Float	N/A	See input config	See config	RO	
30201	1355	0x054B	2	CAN Receive Signal #6	Float	N/A	See input config	See config	RO	
30203	1357	0x054D	2	CAN Receive Signal #7	Float	N/A	See input config	See config	RO	
30205	1359	0x054F	2	CAN Receive Signal #8	Float	N/A	See input config	See config	RO	
30207	1361	0x0551	2	CAN Receive Signal #9	Float	N/A	See input config	See config	RO	
30209	1363	0x0553	2	CAN Receive Signal #10	Float	N/A	See input config	See config	RO	
30211	1365	0x0555	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Auxiliary Signals										
30377	1377	0x0561	2	Global Discrete Constant Signal	Dword	N/A	N/A	N/A	RO	Equals to the Global Discrete Constant Signal configuration parameter
30379	1379	0x0563	2	Global Continuous Constant Signal	Float	N/A	N/A	N/A	RO	Equals to the Global Continuous Constant Signal configuration parameter

301382	1381	0x0565	2	Supply Voltage	Float	N/A	Not Rated	V	RO	Covers rated supply voltage range
301384	1383	0x0567	2	Microcontroller Temperature	Float	N/A	Not Rated	Deg.C	RO	Covers rated temperature range
Configuration Section										
Universal Input #1										
401025	1024	0x0400	1	Input Type	Word	1 - Voltage	0 - Disabled 1 - Voltage Input 2 - Current Input 3 - Discrete Voltage Level 4 - Frequency 5 - PWM Duty Cycle	N/A	R/W	
401026	1025	0x0401	1	Voltage Range	Word	0 - 0…5V	See output config	See config	R/W	
401027	1026	0x0402	1	Current Range	Word	0 - 0…20mA	See output config	See config	R/W	
401028	1027	0x0403	2	Input Range Min	Float	0	See output config	See config	R/W	
401030	1029	0x0405	2	Input Range Max	Float	5	See output config	See config	R/W	
401032	1031	0x0407	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401033	1032	0x0408	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401034	1033	0x0409	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401035	1034	0x040A	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401036	1035	0x040B	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401037	1036	0x040C	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401038	1037	0x040D	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401039	1038	0x040E	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401040	1039	0x040F	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401041	1040	0x0410	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401042	1041	0x0411	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401043	1042	0x0412	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401045	1044	0x0414	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	

401046	1045	0x0415	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401047	1046	0x0416	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401048	1047	0x0417	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401049	1048	0x0418	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401050	1049	0x0419	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401052	1051	0x041B	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401053	1052	0x041C	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401054	1053	0x041D	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Universal Input #2										
401062	1061	0x0425	1	Input Type	Word	1 - Voltage	0 - Disabled 1 - Voltage Input 2 - Current Input 3 - Discrete Voltage Level 4 - Frequency 5 - PWM Duty Cycle	N/A	R/W	
401063	1062	0x0426	1	Voltage Range	Word	0 - 0…5V	See output config	See config	R/W	
401064	1063	0x0427	1	Current Range	Word	0 - 0…20mA	See output config	See config	R/W	
401065	1064	0x0428	2	Input Range Min	Float	0	See output config	See config	R/W	
401067	1066	0x042A	2	Input Range Max	Float	5	See output config	See config	R/W	
401069	1068	0x042C	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401070	1069	0x042D	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401071	1070	0x042E	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401072	1071	0x042F	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401073	1072	0x0430	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401074	1073	0x0431	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401075	1074	0x0432	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	

401076	1075	0x0433	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401077	1076	0x0434	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401078	1077	0x0435	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401079	1078	0x0436	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401080	1079	0x0437	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401082	1081	0x0439	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401083	1082	0x043A	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401084	1083	0x043B	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401085	1084	0x043C	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401086	1085	0x043D	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401087	1086	0x043E	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401089	1088	0x0440	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401090	1089	0x0441	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401091	1090	0x0442	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Universal Input #3										
401099	1098	0x044A	1	Input Type	Word	1 - Voltage	0 - Disabled 1 - Voltage Input 2 - Current Input 3 - Discrete Voltage Level 4 - Frequency 5 - PWM Duty Cycle	N/A	R/W	
401100	1099	0x044B	1	Voltage Range	Word	0 - 0…5V	See output config	See config	R/W	
401101	1100	0x044C	1	Current Range	Word	0 - 0…20mA	See output config	See config	R/W	
401102	1101	0x044D	2	Input Range Min	Float	0	See output config	See config	R/W	

401104	1103	0x044F	2	Input Range Max	Float	5	See output config	See config	R/W	
401106	1105	0x0451	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401107	1106	0x0452	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401108	1107	0x0453	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401109	1108	0x0454	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401110	1109	0x0455	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401111	1110	0x0456	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401112	1111	0x0457	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401113	1112	0x0458	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401114	1113	0x0459	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401115	1114	0x045A	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401116	1115	0x045B	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401117	1116	0x045C	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401119	1118	0x045E	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401120	1119	0x045F	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401121	1120	0x0460	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401122	1121	0x0461	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401123	1122	0x0462	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401124	1123	0x0463	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401126	1125	0x0465	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401127	1126	0x0466	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401128	1127	0x0467	#REF!	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Universal Input #4										

401136	1135	0x046F	1	Input Type	Word	1 - Voltage	0 - Disabled 1 - Voltage Input 2 - Current Input 3 - Discrete Voltage Level 4 - Frequency 5 - PWM Duty Cycle	N/A	R/W	
401137	1136	0x0470	1	Voltage Range	Word	0 - 0…5V	See output config	See config	R/W	
401138	1137	0x0471	1	Current Range	Word	0 - 0…20mA	See output config	See config	R/W	
401139	1138	0x0472	2	Input Range Min	Float	0	See output config	See config	R/W	
401141	1140	0x0474	2	Input Range Max	Float	5	See output config	See config	R/W	
401143	1142	0x0476	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401144	1143	0x0477	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401145	1144	0x0478	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401146	1145	0x0479	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401147	1146	0x047A	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401148	1147	0x047B	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401149	1148	0x047C	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401150	1149	0x047D	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401151	1150	0x047E	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401152	1151	0x047F	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401153	1152	0x0480	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401154	1153	0x0481	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401156	1155	0x0483	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401157	1156	0x0484	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401158	1157	0x0485	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401159	1158	0x0486	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	

401160	1159	0x0487	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401161	1160	0x0488	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401163	1162	0x048A	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401164	1163	0x048B	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401165	1164	0x048C	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
RTD Block #1										
401173	1172	0x0494	1	Input Type	Word	2 - Three Wires	0 - Disabled 1 - Two Wires 2 - Three Wires	N/A	R/W	
401174	1173	0x0495	2	Callendar Van Dusen Constant A	Float	3.9083	See output config	See config	R/W	
401176	1175	0x0497	2	Callendar Van Dusen Constant B	Float	-5.775	See output config	See config	R/W	
401178	1177	0x0499	2	Callendar Van Dusen Constant C	Float	-4.18301	See output config	See config	R/W	
401180	1179	0x049B	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401182	1181	0x049D	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	
401184	1183	0x049F	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401186	1185	0x04A1	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401188	1187	0x04A3	2	High Warning Resistance	Float	200	See output config	See config	R/W	
401190	1189	0x04A5	2	Low Warning Resistance	Float	0	See output config	See config	R/W	
401192	1191	0x04A7	2	High Warning Code	Float	0	See output config	See config	R/W	
401194	1193	0x04A9	2	Low Warning Code	Float	0	See output config	See config	R/W	
401196	1195	0x04AB	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401197	1196	0x04AC	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401198	1197	0x04AD	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401199	1198	0x04AE	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401201	1200	0x04B0	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401202	1201	0x04B1	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

401203	1202	0x04B2	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401204	1203	0x04B3	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401205	1204	0x04B4	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401206	1205	0x04B5	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401208	1207	0x04B7	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401209	1208	0x04B8	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401210	1209	0x04B9	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401211	1210	0x04BA	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401212	1211	0x04BB	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401213	1212	0x04BC	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401215	1214	0x04BE	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401216	1215	0x04BF	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401217	1216	0x04C0	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401218	1217	0x04C1	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401219	1218	0x04C2	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401220	1219	0x04C3	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401222	1221	0x04C5	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401223	1222	0x04C6	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401224	1223	0x04C7	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed

										but does not change the value.
RTD Block #2										
401231	1230	0x04CE	1	Input Type	Word	2 - Three Wires	0 - Disabled 1 - Two Wires 2 - Three Wires	N/A	R/W	
401232	1231	0x04CF	2	Callendar Van Dusen Constant A	Float	3.9083	See output config	See config	R/W	
401234	1233	0x04D1	2	Callendar Van Dusen Constant B	Float	-5.775	See output config	See config	R/W	
401236	1235	0x04D3	2	Callendar Van Dusen Constant C	Float	-4.18301	See output config	See config	R/W	
401238	1237	0x04D5	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401240	1239	0x04D7	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	
401242	1241	0x04D9	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401244	1243	0x04DB	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401246	1245	0x04DD	2	High Warning Resistance	Float	200	See output config	See config	R/W	
401248	1247	0x04DF	2	Low Warning Resistance	Float	0	See output config	See config	R/W	
401250	1249	0x04E1	2	High Warning Code	Float	0	See output config	See config	R/W	
401252	1251	0x04E3	2	Low Warning Code	Float	0	See output config	See config	R/W	
401254	1253	0x04E5	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401255	1254	0x04E6	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401256	1255	0x04E7	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401257	1256	0x04E8	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401259	1258	0x04EA	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401260	1259	0x04EB	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401261	1260	0x04EC	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401262	1261	0x04ED	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401263	1262	0x04EE	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401264	1263	0x04EF	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401266	1265	0x04F1	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	

401267	1266	0x04F2	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401268	1267	0x04F3	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401269	1268	0x04F4	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401270	1269	0x04F5	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401271	1270	0x04F6	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401273	1272	0x04F8	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401274	1273	0x04F9	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401275	1274	0x04FA	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401276	1275	0x04FB	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401277	1276	0x04FC	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401278	1277	0x04FD	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401280	1279	0x04FF	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401281	1280	0x0500	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401282	1281	0x0501	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #1										
401289	1288	0x0508	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
401290	1289	0x0509	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
401291	1290	0x050A	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	

401293	1292	0x050C	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	
401295	1294	0x050E	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401297	1296	0x0510	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401299	1298	0x0512	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401301	1300	0x0514	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401303	1302	0x0516	2	High Warning Code	Float	0	See output config	See config	R/W	
401305	1304	0x0518	2	Low Warning Code	Float	0	See output config	See config	R/W	
401307	1306	0x051A	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401308	1307	0x051B	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401309	1308	0x051C	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401310	1309	0x051D	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401312	1311	0x051F	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401313	1312	0x0520	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401314	1313	0x0521	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401315	1314	0x0522	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401316	1315	0x0523	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401317	1316	0x0524	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401319	1318	0x0526	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401320	1319	0x0527	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401321	1320	0x0528	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401322	1321	0x0529	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401323	1322	0x052A	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401324	1323	0x052B	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401326	1325	0x052D	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401327	1326	0x052E	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

401328	1327	0x052F	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401329	1328	0x0530	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401330	1329	0x0531	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401331	1330	0x0532	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401333	1332	0x0534	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401334	1333	0x0535	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401335	1334	0x0536	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401336	1335	0x0537	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401337	1336	0x0538	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401338	1337	0x0539	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401340	1339	0x053B	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401341	1340	0x053C	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401342	1341	0x053D	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #2										
401347	1346	0x0542	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
401348	1347	0x0543	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
401349	1348	0x0544	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401351	1350	0x0546	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401353	1352	0x0548	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401355	1354	0x054A	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401357	1356	0x054C	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401359	1358	0x054E	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401361	1360	0x0550	2	High Warning Code	Float	0	See output config	See config	R/W	
401363	1362	0x0552	2	Low Warning Code	Float	0	See output config	See config	R/W	
401365	1364	0x0554	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401366	1365	0x0555	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401367	1366	0x0556	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401368	1367	0x0557	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401370	1369	0x0559	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401371	1370	0x055A	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401372	1371	0x055B	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401373	1372	0x055C	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401374	1373	0x055D	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401375	1374	0x055E	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401377	1376	0x0560	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401378	1377	0x0561	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401379	1378	0x0562	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401380	1379	0x0563	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401381	1380	0x0564	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401382	1381	0x0565	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401384	1383	0x0567	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401385	1384	0x0568	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

401386	1385	0x0569	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401387	1386	0x056A	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401388	1387	0x056B	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401389	1388	0x056C	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401391	1390	0x056E	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401392	1391	0x056F	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401393	1392	0x0570	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401394	1393	0x0571	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401395	1394	0x0572	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401396	1395	0x0573	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401398	1397	0x0575	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401399	1398	0x0576	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401400	1399	0x0577	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #3										
401405	1404	0x057C	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
401406	1405	0x057D	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
401407	1406	0x057E	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401409	1408	0x0580	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401411	1410	0x0582	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401413	1412	0x0584	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401415	1414	0x0586	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401417	1416	0x0588	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401419	1418	0x058A	2	High Warning Code	Float	0	See output config	See config	R/W	
401421	1420	0x058C	2	Low Warning Code	Float	0	See output config	See config	R/W	
401423	1422	0x058E	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401424	1423	0x058F	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401425	1424	0x0590	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401426	1425	0x0591	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401428	1427	0x0593	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401429	1428	0x0594	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401430	1429	0x0595	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401431	1430	0x0596	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401432	1431	0x0597	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401433	1432	0x0598	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401435	1434	0x059A	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401436	1435	0x059B	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401437	1436	0x059C	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401438	1437	0x059D	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401439	1438	0x059E	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401440	1439	0x059F	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401442	1441	0x05A1	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401443	1442	0x05A2	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

40144	1443	0x05A3	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40145	1444	0x05A4	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40146	1445	0x05A5	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40147	1446	0x05A6	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40149	1448	0x05A8	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40150	1449	0x05A9	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40151	1450	0x05AA	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40152	1451	0x05AB	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40153	1452	0x05AC	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40154	1453	0x05AD	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40156	1455	0x05AF	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40157	1456	0x05B0	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40158	1457	0x05B1	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #4										
40163	1462	0x05B6	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
40164	1463	0x05B7	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
40165	1464	0x05B8	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
40167	1466	0x05BA	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401469	1468	0x05BC	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401471	1470	0x05BE	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401473	1472	0x05C0	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401475	1474	0x05C2	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401477	1476	0x05C4	2	High Warning Code	Float	0	See output config	See config	R/W	
401479	1478	0x05C6	2	Low Warning Code	Float	0	See output config	See config	R/W	
401481	1480	0x05C8	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401482	1481	0x05C9	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401483	1482	0x05CA	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401484	1483	0x05CB	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401486	1485	0x05CD	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401487	1486	0x05CE	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401488	1487	0x05CF	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401489	1488	0x05D0	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401490	1489	0x05D1	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401491	1490	0x05D2	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401493	1492	0x05D4	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401494	1493	0x05D5	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401495	1494	0x05D6	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401496	1495	0x05D7	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401497	1496	0x05D8	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401498	1497	0x05D9	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401500	1499	0x05DB	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401501	1500	0x05DC	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

401502	1501	0x05DD	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401503	1502	0x05DE	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401504	1503	0x05DF	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401505	1504	0x05E0	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401507	1506	0x05E2	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401508	1507	0x05E3	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401509	1508	0x05E4	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401510	1509	0x05E5	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401511	1510	0x05E6	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401512	1511	0x05E7	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401514	1513	0x05E9	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401515	1514	0x05EA	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401516	1515	0x05EB	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #5										
401521	1520	0x05F0	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
401522	1521	0x05F1	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
401523	1522	0x05F2	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401525	1524	0x05F4	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401527	1526	0x05F6	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401529	1528	0x05F8	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401531	1530	0x05FA	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401533	1532	0x05FC	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401535	1534	0x05FE	2	High Warning Code	Float	0	See output config	See config	R/W	
401537	1536	0x0600	2	Low Warning Code	Float	0	See output config	See config	R/W	
401539	1538	0x0602	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401540	1539	0x0603	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401541	1540	0x0604	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401542	1541	0x0605	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401544	1543	0x0607	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401545	1544	0x0608	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401546	1545	0x0609	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401547	1546	0x060A	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401548	1547	0x060B	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401549	1548	0x060C	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401551	1550	0x060E	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401552	1551	0x060F	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401553	1552	0x0610	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401554	1553	0x0611	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401555	1554	0x0612	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401556	1555	0x0613	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401558	1557	0x0615	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401559	1558	0x0616	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

40156	1559	0x0617	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40161	1560	0x0618	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40162	1561	0x0619	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40163	1562	0x061A	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40165	1564	0x061C	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40166	1565	0x061D	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40167	1566	0x061E	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40168	1567	0x061F	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40169	1568	0x0620	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40170	1569	0x0621	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40172	1571	0x0623	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40173	1572	0x0624	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40174	1573	0x0625	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #6										
40179	1578	0x062A	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
40180	1579	0x062B	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
40181	1580	0x062C	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
40183	1582	0x062E	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401585	1584	0x0630	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401587	1586	0x0632	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401589	1588	0x0634	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401591	1590	0x0636	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401593	1592	0x0638	2	High Warning Code	Float	0	See output config	See config	R/W	
401595	1594	0x063A	2	Low Warning Code	Float	0	See output config	See config	R/W	
401597	1596	0x063C	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401598	1597	0x063D	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401599	1598	0x063E	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401600	1599	0x063F	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401602	1601	0x0641	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401603	1602	0x0642	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401604	1603	0x0643	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401605	1604	0x0644	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401606	1605	0x0645	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401607	1606	0x0646	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401609	1608	0x0648	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401610	1609	0x0649	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401611	1610	0x064A	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401612	1611	0x064B	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401613	1612	0x064C	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401614	1613	0x064D	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401616	1615	0x064F	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401617	1616	0x0650	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

40161 8	1617	0x0651	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40161 9	1618	0x0652	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40162 0	1619	0x0653	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40162 1	1620	0x0654	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40162 3	1622	0x0656	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40162 4	1623	0x0657	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40162 5	1624	0x0658	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40162 6	1625	0x0659	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40162 7	1626	0x065A	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40162 8	1627	0x065B	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40163 0	1629	0x065D	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40163 1	1630	0x065E	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40163 2	1631	0x065F	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #7										
40163 7	1636	0x0664	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
40163 8	1637	0x0665	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
40163 9	1638	0x0666	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
40164 1	1640	0x0668	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401643	1642	0x066A	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401645	1644	0x066C	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401647	1646	0x066E	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401649	1648	0x0670	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401651	1650	0x0672	2	High Warning Code	Float	0	See output config	See config	R/W	
401653	1652	0x0674	2	Low Warning Code	Float	0	See output config	See config	R/W	
401655	1654	0x0676	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401656	1655	0x0677	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401657	1656	0x0678	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401658	1657	0x0679	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401660	1659	0x067B	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401661	1660	0x067C	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401662	1661	0x067D	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401663	1662	0x067E	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401664	1663	0x067F	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401665	1664	0x0680	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401667	1666	0x0682	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401668	1667	0x0683	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401669	1668	0x0684	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401670	1669	0x0685	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401671	1670	0x0686	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401672	1671	0x0687	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401674	1673	0x0689	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401675	1674	0x068A	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

40167	1675	0x068B	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40167	1676	0x068C	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40167	1677	0x068D	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40167	1678	0x068E	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40168	1680	0x0690	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40168	1681	0x0691	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40168	1682	0x0692	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40168	1683	0x0693	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40168	1684	0x0694	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40168	1685	0x0695	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40168	1687	0x0697	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40168	1688	0x0698	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40169	1689	0x0699	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #8										
40169	1694	0x069E	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
40169	1695	0x069F	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
40169	1696	0x06A0	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
40169	1698	0x06A2	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401701	1700	0x06A4	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401703	1702	0x06A6	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401705	1704	0x06A8	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401707	1706	0x06AA	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401709	1708	0x06AC	2	High Warning Code	Float	0	See output config	See config	R/W	
401711	1710	0x06AE	2	Low Warning Code	Float	0	See output config	See config	R/W	
401713	1712	0x06B0	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401714	1713	0x06B1	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401715	1714	0x06B2	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401716	1715	0x06B3	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401718	1717	0x06B5	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401719	1718	0x06B6	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401720	1719	0x06B7	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401721	1720	0x06B8	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401722	1721	0x06B9	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401723	1722	0x06BA	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401725	1724	0x06BC	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401726	1725	0x06BD	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401727	1726	0x06BE	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401728	1727	0x06BF	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401729	1728	0x06C0	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401730	1729	0x06C1	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401732	1731	0x06C3	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401733	1732	0x06C4	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

40173 4	1733	0x06C5	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40173 5	1734	0x06C6	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40173 6	1735	0x06C7	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40173 7	1736	0x06C8	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40173 9	1738	0x06CA	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40174 0	1739	0x06CB	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40174 1	1740	0x06CC	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40174 2	1741	0x06CD	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40174 3	1742	0x06CE	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40174 4	1743	0x06CF	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40174 6	1745	0x06D1	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40174 7	1746	0x06D2	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40174 8	1747	0x06D3	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #9										
40175 3	1752	0x06D8	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
40175 4	1753	0x06D9	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
40175 5	1754	0x06DA	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
40175 7	1756	0x06DC	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401759	1758	0x06DE	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401761	1760	0x06E0	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401763	1762	0x06E2	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401765	1764	0x06E4	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401767	1766	0x06E6	2	High Warning Code	Float	0	See output config	See config	R/W	
401769	1768	0x06E8	2	Low Warning Code	Float	0	See output config	See config	R/W	
401771	1770	0x06EA	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401772	1771	0x06EB	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401773	1772	0x06EC	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401774	1773	0x06ED	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401776	1775	0x06EF	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401777	1776	0x06F0	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401778	1777	0x06F1	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401779	1778	0x06F2	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401780	1779	0x06F3	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401781	1780	0x06F4	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401783	1782	0x06F6	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401784	1783	0x06F7	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401785	1784	0x06F8	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401786	1785	0x06F9	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401787	1786	0x06FA	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401788	1787	0x06FB	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401790	1789	0x06FD	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401791	1790	0x06FE	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

401792	1791	0x06FF	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401793	1792	0x0700	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401794	1793	0x0701	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401795	1794	0x0702	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401797	1796	0x0704	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401798	1797	0x0705	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401799	1798	0x0706	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401800	1799	0x0707	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401801	1800	0x0708	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401802	1801	0x0709	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401804	1803	0x070B	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401805	1804	0x070C	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401806	1805	0x070D	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #10										
401811	1810	0x0712	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
401812	1811	0x0713	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
401813	1812	0x0714	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401815	1814	0x0716	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401817	1816	0x0718	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401819	1818	0x071A	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401821	1820	0x071C	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401823	1822	0x071E	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401825	1824	0x0720	2	High Warning Code	Float	0	See output config	See config	R/W	
401827	1826	0x0722	2	Low Warning Code	Float	0	See output config	See config	R/W	
401829	1828	0x0724	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401830	1829	0x0725	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401831	1830	0x0726	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401832	1831	0x0727	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401834	1833	0x0729	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401835	1834	0x072A	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401836	1835	0x072B	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401837	1836	0x072C	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401838	1837	0x072D	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401839	1838	0x072E	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401841	1840	0x0730	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401842	1841	0x0731	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401843	1842	0x0732	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401844	1843	0x0733	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401845	1844	0x0734	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401846	1845	0x0735	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401848	1847	0x0737	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401849	1848	0x0738	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

401850	1849	0x0739	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401851	1850	0x073A	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401852	1851	0x073B	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401853	1852	0x073C	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401855	1854	0x073E	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401856	1855	0x073F	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401857	1856	0x0740	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401858	1857	0x0741	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401859	1858	0x0742	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401860	1859	0x0743	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401862	1861	0x0745	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401863	1862	0x0746	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401864	1863	0x0747	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #11										
401869	1868	0x074C	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
401870	1869	0x074D	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
401871	1870	0x074E	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401873	1872	0x0750	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401875	1874	0x0752	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401877	1876	0x0754	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401879	1878	0x0756	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401881	1880	0x0758	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401883	1882	0x075A	2	High Warning Code	Float	0	See output config	See config	R/W	
401885	1884	0x075C	2	Low Warning Code	Float	0	See output config	See config	R/W	
401887	1886	0x075E	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401888	1887	0x075F	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401889	1888	0x0760	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401890	1889	0x0761	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401892	1891	0x0763	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401893	1892	0x0764	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401894	1893	0x0765	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401895	1894	0x0766	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401896	1895	0x0767	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401897	1896	0x0768	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401899	1898	0x076A	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401900	1899	0x076B	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401901	1900	0x076C	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401902	1901	0x076D	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401903	1902	0x076E	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401904	1903	0x076F	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401906	1905	0x0771	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401907	1906	0x0772	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

401908	1907	0x0773	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401909	1908	0x0774	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401910	1909	0x0775	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401911	1910	0x0776	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401913	1912	0x0778	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401914	1913	0x0779	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401915	1914	0x077A	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401916	1915	0x077B	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401917	1916	0x077C	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401918	1917	0x077D	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401920	1919	0x077F	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401921	1920	0x0780	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401922	1921	0x0781	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #12										
401927	1926	0x0786	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
401928	1927	0x0787	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
401929	1928	0x0788	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401931	1930	0x078A	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401933	1932	0x078C	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401935	1934	0x078E	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401937	1936	0x0790	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401939	1938	0x0792	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401941	1940	0x0794	2	High Warning Code	Float	0	See output config	See config	R/W	
401943	1942	0x0796	2	Low Warning Code	Float	0	See output config	See config	R/W	
401945	1944	0x0798	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401946	1945	0x0799	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401947	1946	0x079A	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401948	1947	0x079B	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
401950	1949	0x079D	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
401951	1950	0x079E	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401952	1951	0x079F	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401953	1952	0x07A0	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401954	1953	0x07A1	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401955	1954	0x07A2	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
401957	1956	0x07A4	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
401958	1957	0x07A5	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401959	1958	0x07A6	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401960	1959	0x07A7	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401961	1960	0x07A8	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401962	1961	0x07A9	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
401964	1963	0x07AB	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
401965	1964	0x07AC	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

401966	1965	0x07AD	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401967	1966	0x07AE	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401968	1967	0x07AF	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401969	1968	0x07B0	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401971	1970	0x07B2	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401972	1971	0x07B3	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401973	1972	0x07B4	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
401974	1973	0x07B5	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
401975	1974	0x07B6	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
401976	1975	0x07B7	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
401978	1977	0x07B9	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
401979	1978	0x07BA	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
401980	1979	0x07BB	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #13										
401985	1984	0x07C0	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
401986	1985	0x07C1	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
401987	1986	0x07C2	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
401989	1988	0x07C4	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

401991	1990	0x07C6	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
401993	1992	0x07C8	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
401995	1994	0x07CA	2	High Warning Voltage	Float	70	See output config	See config	R/W	
401997	1996	0x07CC	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
401999	1998	0x07CE	2	High Warning Code	Float	0	See output config	See config	R/W	
402001	2000	0x07D0	2	Low Warning Code	Float	0	See output config	See config	R/W	
402003	2002	0x07D2	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402004	2003	0x07D3	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402005	2004	0x07D4	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402006	2005	0x07D5	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
402008	2007	0x07D7	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
402009	2008	0x07D8	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402010	2009	0x07D9	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402011	2010	0x07DA	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402012	2011	0x07DB	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402013	2012	0x07DC	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
402015	2014	0x07DE	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
402016	2015	0x07DF	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402017	2016	0x07E0	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402018	2017	0x07E1	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402019	2018	0x07E2	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402020	2019	0x07E3	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
402022	2021	0x07E5	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
402023	2022	0x07E6	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

402024	2023	0x07E7	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402025	2024	0x07E8	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402026	2025	0x07E9	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402027	2026	0x07EA	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402029	2028	0x07EC	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402030	2029	0x07ED	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402031	2030	0x07EE	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402032	2031	0x07EF	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402033	2032	0x07F0	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402034	2033	0x07F1	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402036	2035	0x07F3	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402037	2036	0x07F4	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402038	2037	0x07F5	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #14										
402043	2042	0x07FA	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
402044	2043	0x07FB	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
402045	2044	0x07FC	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
402047	2046	0x07FE	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

402049	2048	0x0800	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
402051	2050	0x0802	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
402053	2052	0x0804	2	High Warning Voltage	Float	70	See output config	See config	R/W	
402055	2054	0x0806	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
402057	2056	0x0808	2	High Warning Code	Float	0	See output config	See config	R/W	
402059	2058	0x080A	2	Low Warning Code	Float	0	See output config	See config	R/W	
402061	2060	0x080C	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402062	2061	0x080D	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402063	2062	0x080E	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402064	2063	0x080F	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
402066	2065	0x0811	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
402067	2066	0x0812	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402068	2067	0x0813	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402069	2068	0x0814	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402070	2069	0x0815	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402071	2070	0x0816	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
402073	2072	0x0818	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
402074	2073	0x0819	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402075	2074	0x081A	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402076	2075	0x081B	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402077	2076	0x081C	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402078	2077	0x081D	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
402080	2079	0x081F	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
402081	2080	0x0820	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

402082	2081	0x0821	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402083	2082	0x0822	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402084	2083	0x0823	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402085	2084	0x0824	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402087	2086	0x0826	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402088	2087	0x0827	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402089	2088	0x0828	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402090	2089	0x0829	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402091	2090	0x082A	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402092	2091	0x082B	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402094	2093	0x082D	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402095	2094	0x082E	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402096	2095	0x082F	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #15										
402101	2100	0x0834	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
402102	2101	0x0835	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
402103	2102	0x0836	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
402105	2104	0x0838	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

402107	2106	0x083A	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
402109	2108	0x083C	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
402111	2110	0x083E	2	High Warning Voltage	Float	70	See output config	See config	R/W	
402113	2112	0x0840	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
402115	2114	0x0842	2	High Warning Code	Float	0	See output config	See config	R/W	
402117	2116	0x0844	2	Low Warning Code	Float	0	See output config	See config	R/W	
402119	2118	0x0846	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402120	2119	0x0847	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402121	2120	0x0848	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402122	2121	0x0849	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
402124	2123	0x084B	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
402125	2124	0x084C	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402126	2125	0x084D	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402127	2126	0x084E	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402128	2127	0x084F	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402129	2128	0x0850	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
402131	2130	0x0852	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
402132	2131	0x0853	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402133	2132	0x0854	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402134	2133	0x0855	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402135	2134	0x0856	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402136	2135	0x0857	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
402138	2137	0x0859	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
402139	2138	0x085A	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

402140	2139	0x085B	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402141	2140	0x085C	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402142	2141	0x085D	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402143	2142	0x085E	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402145	2144	0x0860	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402146	2145	0x0861	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402147	2146	0x0862	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402148	2147	0x0863	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402149	2148	0x0864	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402150	2149	0x0865	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402152	2151	0x0867	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402153	2152	0x0868	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402154	2153	0x0869	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #16										
402159	2158	0x086E	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
402160	2159	0x086F	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
402161	2160	0x0870	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
402163	2162	0x0872	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

402165	2164	0x0874	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
402167	2166	0x0876	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
402169	2168	0x0878	2	High Warning Voltage	Float	70	See output config	See config	R/W	
402171	2170	0x087A	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
402173	2172	0x087C	2	High Warning Code	Float	0	See output config	See config	R/W	
402175	2174	0x087E	2	Low Warning Code	Float	0	See output config	See config	R/W	
402177	2176	0x0880	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402178	2177	0x0881	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402179	2178	0x0882	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402180	2179	0x0883	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
402182	2181	0x0885	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
402183	2182	0x0886	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402184	2183	0x0887	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402185	2184	0x0888	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402186	2185	0x0889	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402187	2186	0x088A	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
402189	2188	0x088C	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
402190	2189	0x088D	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402191	2190	0x088E	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402192	2191	0x088F	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402193	2192	0x0890	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402194	2193	0x0891	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
402196	2195	0x0893	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
402197	2196	0x0894	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

402198	2197	0x0895	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402199	2198	0x0896	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402200	2199	0x0897	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402201	2200	0x0898	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402203	2202	0x089A	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402204	2203	0x089B	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402205	2204	0x089C	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402206	2205	0x089D	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402207	2206	0x089E	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402208	2207	0x089F	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402210	2209	0x08A1	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402211	2210	0x08A2	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402212	2211	0x08A3	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #17										
402217	2216	0x08A8	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
402218	2217	0x08A9	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
402219	2218	0x08AA	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
402221	2220	0x08AC	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

40222	2222	0x08AE	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
40225	2224	0x08B0	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
40227	2226	0x08B2	2	High Warning Voltage	Float	70	See output config	See config	R/W	
40229	2228	0x08B4	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
40231	2230	0x08B6	2	High Warning Code	Float	0	See output config	See config	R/W	
40233	2232	0x08B8	2	Low Warning Code	Float	0	See output config	See config	R/W	
40235	2234	0x08BA	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40236	2235	0x08BB	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40237	2236	0x08BC	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40238	2237	0x08BD	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
40240	2239	0x08BF	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
40241	2240	0x08C0	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40242	2241	0x08C1	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40243	2242	0x08C2	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40244	2243	0x08C3	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40245	2244	0x08C4	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
40247	2246	0x08C6	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
40248	2247	0x08C7	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40249	2248	0x08C8	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40250	2249	0x08C9	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40251	2250	0x08CA	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40252	2251	0x08CB	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
40254	2253	0x08CD	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
40255	2254	0x08CE	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

40225	2255	0x08CF	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40226	2256	0x08D0	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40227	2257	0x08D1	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40228	2258	0x08D2	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40230	2260	0x08D4	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40231	2261	0x08D5	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40232	2262	0x08D6	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40233	2263	0x08D7	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40234	2264	0x08D8	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40235	2265	0x08D9	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40237	2267	0x08DB	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40238	2268	0x08DC	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40239	2269	0x08DD	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #18										
40274	2274	0x08E2	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
40275	2275	0x08E3	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
40276	2276	0x08E4	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
40278	2278	0x08E6	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

402281	2280	0x08E8	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
402283	2282	0x08EA	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
402285	2284	0x08EC	2	High Warning Voltage	Float	70	See output config	See config	R/W	
402287	2286	0x08EE	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
402289	2288	0x08F0	2	High Warning Code	Float	0	See output config	See config	R/W	
402291	2290	0x08F2	2	Low Warning Code	Float	0	See output config	See config	R/W	
402293	2292	0x08F4	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402294	2293	0x08F5	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402295	2294	0x08F6	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402296	2295	0x08F7	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
402298	2297	0x08F9	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
402299	2298	0x08FA	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402300	2299	0x08FB	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402301	2300	0x08FC	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402302	2301	0x08FD	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402303	2302	0x08FE	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
402305	2304	0x0900	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
402306	2305	0x0901	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402307	2306	0x0902	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402308	2307	0x0903	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402309	2308	0x0904	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402310	2309	0x0905	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
402312	2311	0x0907	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
402313	2312	0x0908	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

40231	2313	0x0909	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40231	2314	0x090A	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40231	2315	0x090B	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40231	2316	0x090C	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40231	2318	0x090E	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40232	2319	0x090F	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40232	2320	0x0910	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40232	2321	0x0911	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40232	2322	0x0912	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40232	2323	0x0913	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40232	2325	0x0915	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40232	2326	0x0916	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40232	2327	0x0917	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #19										
40233	2332	0x091C	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
40234	2333	0x091D	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
40235	2334	0x091E	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
40237	2336	0x0920	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

402339	2338	0x0922	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
402341	2340	0x0924	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
402343	2342	0x0926	2	High Warning Voltage	Float	70	See output config	See config	R/W	
402345	2344	0x0928	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
402347	2346	0x092A	2	High Warning Code	Float	0	See output config	See config	R/W	
402349	2348	0x092C	2	Low Warning Code	Float	0	See output config	See config	R/W	
402351	2350	0x092E	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402352	2351	0x092F	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402353	2352	0x0930	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402354	2353	0x0931	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
402356	2355	0x0933	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
402357	2356	0x0934	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402358	2357	0x0935	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402359	2358	0x0936	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402360	2359	0x0937	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402361	2360	0x0938	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
402363	2362	0x093A	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
402364	2363	0x093B	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402365	2364	0x093C	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402366	2365	0x093D	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402367	2366	0x093E	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402368	2367	0x093F	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
402370	2369	0x0941	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
402371	2370	0x0942	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

40237	2371	0x0943	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40237	2372	0x0944	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40237	2373	0x0945	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40237	2374	0x0946	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40237	2376	0x0948	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40237	2377	0x0949	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40237	2378	0x094A	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
40237	2379	0x094B	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
40238	2380	0x094C	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
40238	2381	0x094D	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
40238	2383	0x094F	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
40238	2384	0x0950	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
40238	2385	0x0951	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Thermocouple Block #20										
40239	2390	0x0956	1	Input Type	Word	4 - K Type	See output config	N/A	R/W	
40239	2391	0x0957	1	Enable Cold Junction Compensation	Byte	1 - CJ Enabled	See output config	See config	R/W	
40239	2392	0x0958	2	High Shutdown Temperature	Float	1000	See output config	See config	R/W	
40239	2394	0x095A	2	Low Shutdown Temperature	Float	0	See output config	See config	R/W	

402397	2396	0x095C	2	High Warning Temperature	Float	1000	See output config	See config	R/W	
402399	2398	0x095E	2	Low Warning Temperature	Float	0	See output config	See config	R/W	
402401	2400	0x0960	2	High Warning Voltage	Float	70	See output config	See config	R/W	
402403	2402	0x0962	2	Low Warning Voltage	Float	0	See output config	See config	R/W	
402405	2404	0x0964	2	High Warning Code	Float	0	See output config	See config	R/W	
402407	2406	0x0966	2	Low Warning Code	Float	0	See output config	See config	R/W	
402409	2408	0x0968	1	Low Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402410	2409	0x0969	1	Low Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402411	2410	0x096A	1	Lamp Set By Low Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402412	2411	0x096B	2	SPN for Low Limit used in DTC	Double	N/A	See output config	See config	R/W	
402414	2413	0x096D	1	FMI for Low Limit used in DTC	Byte	0	See output config	See config	R/W	
402415	2414	0x096E	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402416	2415	0x096F	1	High Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402417	2416	0x0970	1	High Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402418	2417	0x0971	1	Lamp Set By High Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402419	2418	0x0972	2	SPN for High Limit used in DTC	Double	N/A	See output config	See config	R/W	
402421	2420	0x0974	1	FMI for High Limit used in DTC	Byte	0	See output config	See config	R/W	
402422	2421	0x0975	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402423	2422	0x0976	1	Open Circuit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402424	2423	0x0977	1	Open Circuit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402425	2424	0x0978	1	Lamp Set By Open Circuit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402426	2425	0x0979	2	SPN for Open Circuit used in DTC	Double	N/A	See output config	See config	R/W	
402428	2427	0x097B	1	FMI for Open Circuit used in DTC	Byte	0	See output config	See config	R/W	
402429	2428	0x097C	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	

402430	2429	0x097D	1	Low Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402431	2430	0x097E	1	Low Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402432	2431	0x097F	1	Lamp Set By Low Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402433	2432	0x0980	2	SPN for Low Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402435	2434	0x0982	1	FMI for Low Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402436	2435	0x0983	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402437	2436	0x0984	1	High Shutdown Limit Generates a DTC In DM1	Byte	0 - Disabled	0 - Disabled 1 - Enabled	See config	R/W	
402438	2437	0x0985	1	High Shutdown Limit Cleared Only by DM11	Byte	0 - False	0 - False 1 - True	See config	R/W	
402439	2438	0x0986	1	Lamp Set By High Shutdown Limit in DM1	Byte	0 - Protect	See output config	See config	R/W	
402440	2439	0x0987	2	SPN for High Shutdown Limit used in DTC	Double	N/A	See output config	See config	R/W	
402442	2441	0x0989	1	FMI for High Shutdown Limit used in DTC	Byte	0	See output config	See config	R/W	
402443	2442	0x098A	1	Delay Before Sending DM1	Word	1000[ms]	See output config	See config	R/W	
402444	2443	0x098B	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Relay Output #1										
402449	2448	0x0990	1	Relay Output Type	Byte	0, Disabled	See output config	N/A	R/W	
402450	2449	0x0991	1	Relay Toggle Rate	Word	500	See output config	See config	R/W	
402451	2450	0x0992	1	Relay Control Source	Byte	0 - Control Not Used	See output config	See config	R/W	
402452	2451	0x0993	1	Relay Control Number	Byte	1	See output config	See config	R/W	

40245	2452	0x0994	1	Relay Unlatch Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40245	2453	0x0995	1	Relay Unlatch Number	Byte	1	See output config	See config	R/W	
40245	2454	0x0996	1	Relay Enable Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40245	2455	0x0997	1	Relay Enable Number	Byte	1	See output config	See config	R/W	
40245	2456	0x0998	1	Relay Enable Response	Byte	0 - Enable When ON	See output config	See config	R/W	
40245	2457	0x0999	1	Relay Override Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40245	2458	0x099A	1	Relay Override Number	Byte	1	See output config	See config	R/W	
40246	2459	0x099B	1	Relay Override Response	Byte	0 - Override When ON	See output config	See config	R/W	
40246	2460	0x099C	1	Relay Override State	Byte	0 - Override State ON	See output config	See config	R/W	
40246	2461	0x099D	1	Relay Response Delay	Byte	0 - Disabled	See output config	See config	R/W	
40246	2462	0x099E	1	Relay Delay ON Time	Word	0	See output config	See config	R/W	
40246	2463	0x099F	1	Relay Delay OFF Time	Word	0	See output config	See config	R/W	
40246	2464	0x09A0	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Relay Output #2										
40247	2473	0x09A9	1	Relay Output Type	Byte	0, Disabled	See output config	N/A	R/W	
40247	2474	0x09AA	1	Relay Toggle Rate	Word	500	See output config	See config	R/W	
40247	2475	0x09AB	1	Relay Control Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40247	2476	0x09AC	1	Relay Control Number	Byte	1	See output config	See config	R/W	
40247	2477	0x09AD	1	Relay Unlatch Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40247	2478	0x09AE	1	Relay Unlatch Number	Byte	1	See output config	See config	R/W	
40248	2479	0x09AF	1	Relay Enable Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40248	2480	0x09B0	1	Relay Enable Number	Byte	1	See output config	See config	R/W	
40248	2481	0x09B1	1	Relay Enable Response	Byte	0 - Enable When ON	See output config	See config	R/W	
40248	2482	0x09B2	1	Relay Override Source	Byte	0 - Control Not Used	See output config	See config	R/W	

40248	2483	0x09B3	1	Relay Override Number	Byte	1	See output config	See config	R/W	
40248	2484	0x09B4	1	Relay Override Response	Byte	0 - Override When ON	See output config	See config	R/W	
40248	2485	0x09B5	1	Relay Override State	Byte	0 - Override State ON	See output config	See config	R/W	
40248	2486	0x09B6	1	Relay Response Delay	Byte	0 - Disabled	See output config	See config	R/W	
40248	2487	0x09B7	1	Relay Delay ON Time	Word	0	See output config	See config	R/W	
40248	2488	0x09B8	1	Relay Delay OFF Time	Word	0	See output config	See config	R/W	
40249	2489	0x09B9	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Relay Output #3										
40249	2498	0x09C2	1	Relay Output Type	Byte	0, Disabled	See output config	N/A	R/W	
40250	2499	0x09C3	1	Relay Toggle Rate	Word	500	See output config	See config	R/W	
40250	2500	0x09C4	1	Relay Control Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40250	2501	0x09C5	1	Relay Control Number	Byte	1	See output config	See config	R/W	
40250	2502	0x09C6	1	Relay Unlatch Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40250	2503	0x09C7	1	Relay Unlatch Number	Byte	1	See output config	See config	R/W	
40250	2504	0x09C8	1	Relay Enable Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40250	2505	0x09C9	1	Relay Enable Number	Byte	1	See output config	See config	R/W	
40250	2506	0x09CA	1	Relay Enable Response	Byte	0 - Enable When ON	See output config	See config	R/W	
40250	2507	0x09CB	1	Relay Override Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40250	2508	0x09CC	1	Relay Override Number	Byte	1	See output config	See config	R/W	
40250	2509	0x09CD	1	Relay Override Response	Byte	0 - Override When ON	See output config	See config	R/W	
40251	2510	0x09CE	1	Relay Override State	Byte	0 - Override State ON	See output config	See config	R/W	
40251	2511	0x09CF	1	Relay Response Delay	Byte	0 - Disabled	See output config	See config	R/W	
40251	2512	0x09D0	1	Relay Delay ON Time	Word	0	See output config	See config	R/W	
40251	2513	0x09D1	1	Relay Delay OFF Time	Word	0	See output config	See config	R/W	
40251	2514	0x09D2	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results

										0. Writing is allowed but does not change the value.
Relay Output #4										
40252	2523	0x09DB	1	Relay Output Type	Byte	0, Disabled	See output config	N/A	R/W	
40253	2524	0x09DC	1	Relay Toggle Rate	Word	500	See output config	See config	R/W	
40254	2525	0x09DD	1	Relay Control Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40255	2526	0x09DE	1	Relay Control Number	Byte	1	See output config	See config	R/W	
40256	2527	0x09DF	1	Relay Unlatch Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40257	2528	0x09E0	1	Relay Unlatch Number	Byte	1	See output config	See config	R/W	
40258	2529	0x09E1	1	Relay Enable Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40259	2530	0x09E2	1	Relay Enable Number	Byte	1	See output config	See config	R/W	
40260	2531	0x09E3	1	Relay Enable Response	Byte	0 - Enable When ON	See output config	See config	R/W	
40261	2532	0x09E4	1	Relay Override Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40262	2533	0x09E5	1	Relay Override Number	Byte	1	See output config	See config	R/W	
40263	2534	0x09E6	1	Relay Override Response	Byte	0 - Override When ON	See output config	See config	R/W	
40264	2535	0x09E7	1	Relay Override State	Byte	0 - Override State ON	See output config	See config	R/W	
40265	2536	0x09E8	1	Relay Response Delay	Byte	0 - Disabled	See output config	See config	R/W	
40266	2537	0x09E9	1	Relay Delay ON Time	Word	0	See output config	See config	R/W	
40267	2538	0x09EA	1	Relay Delay OFF Time	Word	0	See output config	See config	R/W	
40268	2539	0x09EB	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Relay Output #5										
40249	2548	0x09F4	1	Relay Output Type	Byte	0, Disabled	See output config	N/A	R/W	
40250	2549	0x09F5	1	Relay Toggle Rate	Word	500	See output config	See config	R/W	
40251	2550	0x09F6	1	Relay Control Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40252	2551	0x09F7	1	Relay Control Number	Byte	1	See output config	See config	R/W	

40253	2552	0x09F8	1	Relay Unlatch Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40254	2553	0x09F9	1	Relay Unlatch Number	Byte	1	See output config	See config	R/W	
40255	2554	0x09FA	1	Relay Enable Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40256	2555	0x09FB	1	Relay Enable Number	Byte	1	See output config	See config	R/W	
40257	2556	0x09FC	1	Relay Enable Response	Byte	0 - Enable When ON	See output config	See config	R/W	
40258	2557	0x09FD	1	Relay Override Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40259	2558	0x09FE	1	Relay Override Number	Byte	1	See output config	See config	R/W	
40260	2559	0x09FF	1	Relay Override Response	Byte	0 - Override When ON	See output config	See config	R/W	
40261	2560	0x0A00	1	Relay Override State	Byte	0 - Override State ON	See output config	See config	R/W	
40262	2561	0x0A01	1	Relay Response Delay	Byte	0 - Disabled	See output config	See config	R/W	
40263	2562	0x0A02	1	Relay Delay ON Time	Word	0	See output config	See config	R/W	
40264	2563	0x0A03	1	Relay Delay OFF Time	Word	0	See output config	See config	R/W	
40265	2564	0x0A04	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Relay Output #6										
40274	2573	0x0A0D	1	Relay Output Type	Byte	0, Disabled	See output config	N/A	R/W	
40275	2574	0x0A0E	1	Relay Toggle Rate	Word	500	See output config	See config	R/W	
40276	2575	0x0A0F	1	Relay Control Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40277	2576	0x0A10	1	Relay Control Number	Byte	1	See output config	See config	R/W	
40278	2577	0x0A11	1	Relay Unlatch Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40279	2578	0x0A12	1	Relay Unlatch Number	Byte	1	See output config	See config	R/W	
40280	2579	0x0A13	1	Relay Enable Source	Byte	0 - Control Not Used	See output config	See config	R/W	
40281	2580	0x0A14	1	Relay Enable Number	Byte	1	See output config	See config	R/W	
40282	2581	0x0A15	1	Relay Enable Response	Byte	0 - Enable When ON	See output config	See config	R/W	
40283	2582	0x0A16	1	Relay Override Source	Byte	0 - Control Not Used	See output config	See config	R/W	

40258	2583	0x0A17	1	Relay Override Number	Byte	1	See output config	See config	R/W	
40258	2584	0x0A18	1	Relay Override Response	Byte	0 - Override When ON	See output config	See config	R/W	
40258	2585	0x0A19	1	Relay Override State	Byte	0 - Override State ON	See output config	See config	R/W	
40258	2586	0x0A1A	1	Relay Response Delay	Byte	0 - Disabled	See output config	See config	R/W	
40258	2587	0x0A1B	1	Relay Delay ON Time	Word	0	See output config	See config	R/W	
40258	2588	0x0A1C	1	Relay Delay OFF Time	Word	0	See output config	See config	R/W	
40259	2589	0x0A1D	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Math Function Block 1										
40259	2598	0x0A26	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
40260	2599	0x0A27	2	Math Output Minimum Range	Float	0	-32768…32767	N/A	R/W	
40260	2601	0x0A29	2	Math Output Maximum Range	Float	100	-32768…32767	N/A	R/W	
40260	2603	0x0A2B	1	Input 1 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40260	2604	0x0A2C	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
40260	2605	0x0A2D	2	Input 1 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40260	2607	0x0A2F	2	Input 1 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40260	2609	0x0A31	2	Input 1 Gain	Float	100	-100…100	N/A	R/W	
40261	2611	0x0A33	1	Input 2 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40261	2612	0x0A34	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
40261	2613	0x0A35	2	Input 2 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40261	2615	0x0A37	2	Input 2 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40261	2617	0x0A39	2	Input 2 Gain	Float	100	-100…100	N/A	R/W	
40261	2619	0x0A3B	1	Input 3 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40262	2620	0x0A3C	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
40262	2621	0x0A3D	2	Input 3 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	

40262 4	2623	0x0A3F	2	Input 3 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40262 6	2625	0x0A41	2	Input 3 Gain	Float	100	-100…100	N/A	R/W	
40262 8	2627	0x0A43	1	Input 4 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40262 9	2628	0x0A44	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
40263 0	2629	0x0A45	2	Input 4 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40263 2	2631	0x0A47	2	Input 4 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40263 4	2633	0x0A49	2	Input 4 Gain	Float	100	-100…100	N/A	R/W	
40263 6	2635	0x0A4B	1	Math Function 1	Byte	0	0..14	N/A	R/W	
40263 7	2636	0x0A4C	1	Math Function 2	Byte	0	0..14	N/A	R/W	
40263 8	2637	0x0A4D	1	Math Function 3	Byte	0	0..14	N/A	R/W	
40263 9	2638	0x0A4E	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Math Function Block 2										
40264 7	2646	0x0A56	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
40264 8	2647	0x0A57	2	Math Output Minimum Range	Float	0	-32768…32767	N/A	R/W	
40265 0	2649	0x0A59	2	Math Output Maximum Range	Float	100	-32768…32767	N/A	R/W	
40265 2	2651	0x0A5B	1	Input 1 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40265 3	2652	0x0A5C	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
40265 4	2653	0x0A5D	2	Input 1 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40265 6	2655	0x0A5F	2	Input 1 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40265 8	2657	0x0A61	2	Input 1 Gain	Float	100	-100…100	N/A	R/W	
40266 0	2659	0x0A63	1	Input 2 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40266 1	2660	0x0A64	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
40266 2	2661	0x0A65	2	Input 2 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40266 4	2663	0x0A67	2	Input 2 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	

402666	2665	0x0A69	2	Input 2 Gain	Float	100	-100…100	N/A	R/W	
402668	2667	0x0A6B	1	Input 3 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402669	2668	0x0A6C	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
402670	2669	0x0A6D	2	Input 3 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402672	2671	0x0A6F	2	Input 3 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402674	2673	0x0A71	2	Input 3 Gain	Float	100	-100…100	N/A	R/W	
402676	2675	0x0A73	1	Input 4 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402677	2676	0x0A74	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
402678	2677	0x0A75	2	Input 4 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402680	2679	0x0A77	2	Input 4 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402682	2681	0x0A79	2	Input 4 Gain	Float	100	-100…100	N/A	R/W	
402684	2683	0x0A7B	1	Math Function 1	Byte	0	0..14	N/A	R/W	
402685	2684	0x0A7C	1	Math Function 2	Byte	0	0..14	N/A	R/W	
402686	2685	0x0A7D	1	Math Function 3	Byte	0	0..14	N/A	R/W	
402687	2686	0x0A7E	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Math Function Block 3										
402695	2694	0x0A86	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
402696	2695	0x0A87	2	Math Output Minimum Range	Float	0	-32768…32767	N/A	R/W	
402698	2697	0x0A89	2	Math Output Maximum Range	Float	100	-32768…32767	N/A	R/W	
402700	2699	0x0A8B	1	Input 1 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402701	2700	0x0A8C	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
402702	2701	0x0A8D	2	Input 1 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402704	2703	0x0A8F	2	Input 1 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402706	2705	0x0A91	2	Input 1 Gain	Float	100	-100…100	N/A	R/W	

402708	2707	0x0A93	1	Input 2 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402709	2708	0x0A94	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
402710	2709	0x0A95	2	Input 2 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402712	2711	0x0A97	2	Input 2 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402714	2713	0x0A99	2	Input 2 Gain	Float	100	-100…100	N/A	R/W	
402716	2715	0x0A9B	1	Input 3 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402717	2716	0x0A9C	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
402718	2717	0x0A9D	2	Input 3 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402720	2719	0x0A9F	2	Input 3 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402722	2721	0x0AA1	2	Input 3 Gain	Float	100	-100…100	N/A	R/W	
402724	2723	0x0AA3	1	Input 4 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402725	2724	0x0AA4	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
402726	2725	0x0AA5	2	Input 4 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402728	2727	0x0AA7	2	Input 4 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402730	2729	0x0AA9	2	Input 4 Gain	Float	100	-100…100	N/A	R/W	
402732	2731	0x0AAB	1	Math Function 1	Byte	0	0..14	N/A	R/W	
402733	2732	0x0AAC	1	Math Function 2	Byte	0	0..14	N/A	R/W	
402734	2733	0x0AAD	1	Math Function 3	Byte	0	0..14	N/A	R/W	
402735	2734	0x0AAE	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Math Function Block 4										
402743	2742	0x0AB6	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
402744	2743	0x0AB7	2	Math Output Minimum Range	Float	0	-32768…32767	N/A	R/W	
402746	2745	0x0AB9	2	Math Output Maximum Range	Float	100	-32768…32767	N/A	R/W	
402748	2747	0x0ABB	1	Input 1 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	

40027 49	2748	0x0ABC	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
40027 50	2749	0x0ABD	2	Input 1 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40027 52	2751	0x0ABF	2	Input 1 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40027 54	2753	0x0AC1	2	Input 1 Gain	Float	100	-100…100	N/A	R/W	
40027 56	2755	0x0AC3	1	Input 2 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40027 57	2756	0x0AC4	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
40027 58	2757	0x0AC5	2	Input 2 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40027 60	2759	0x0AC7	2	Input 2 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40027 62	2761	0x0AC9	2	Input 2 Gain	Float	100	-100…100	N/A	R/W	
40027 64	2763	0x0ACB	1	Input 3 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40027 65	2764	0x0ACC	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
40027 66	2765	0x0ACD	2	Input 3 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40027 68	2767	0x0ACF	2	Input 3 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40027 70	2769	0x0AD1	2	Input 3 Gain	Float	100	-100…100	N/A	R/W	
40027 72	2771	0x0AD3	1	Input 4 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
40027 73	2772	0x0AD4	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
40027 74	2773	0x0AD5	2	Input 4 Minimum	Float	0	$-10^6 \ldots 10^6$	N/A	R/W	
40027 76	2775	0x0AD7	2	Input 4 Maximum	Float	10000	$-10^6 \ldots 10^6$	N/A	R/W	
40027 78	2777	0x0AD9	2	Input 4 Gain	Float	100	-100…100	N/A	R/W	
40027 80	2779	0x0ADB	1	Math Function 1	Byte	0	0..14	N/A	R/W	
40027 81	2780	0x0ADC	1	Math Function 2	Byte	0	0..14	N/A	R/W	
40027 82	2781	0x0ADD	1	Math Function 3	Byte	0	0..14	N/A	R/W	
40027 83	2782	0x0ADE	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Math Function Block 5										

402791	2790	0x0AE6	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
402792	2791	0x0AE7	2	Math Output Minimum Range	Float	0	-32768…32767	N/A	R/W	
402794	2793	0x0AE9	2	Math Output Maximum Range	Float	100	-32768…32767	N/A	R/W	
402796	2795	0x0AEB	1	Input 1 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402797	2796	0x0AEC	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
402798	2797	0x0AED	2	Input 1 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402800	2799	0x0AEF	2	Input 1 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402802	2801	0x0AF1	2	Input 1 Gain	Float	100	-100…100	N/A	R/W	
402804	2803	0x0AF3	1	Input 2 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402805	2804	0x0AF4	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
402806	2805	0x0AF5	2	Input 2 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402808	2807	0x0AF7	2	Input 2 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402810	2809	0x0AF9	2	Input 2 Gain	Float	100	-100…100	N/A	R/W	
402812	2811	0x0AFB	1	Input 3 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402813	2812	0x0AFC	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
402814	2813	0x0AFD	2	Input 3 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402816	2815	0x0AFF	2	Input 3 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402818	2817	0x0B01	2	Input 3 Gain	Float	100	-100…100	N/A	R/W	
402820	2819	0x0B03	1	Input 4 Source	Byte	0 - Control Not Used	0…12	N/A	R/W	
402821	2820	0x0B04	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
402822	2821	0x0B05	2	Input 4 Minimum	Float	0	$-10^6…10^6$	N/A	R/W	
402824	2823	0x0B07	2	Input 4 Maximum	Float	10000	$-10^6…10^6$	N/A	R/W	
402826	2825	0x0B09	2	Input 4 Gain	Float	100	-100…100	N/A	R/W	
402828	2827	0x0B0B	1	Math Function 1	Byte	0	0..14	N/A	R/W	
402829	2828	0x0B0C	1	Math Function 2	Byte	0	0..14	N/A	R/W	

40283	2829	0x0B0D	1	Math Function 3	Byte	0	0..14	N/A	R/W	
40284	2830	0x0B0E	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 1										
40285	2838	0x0B16	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40286	2839	0x0B17	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40287	2840	0x0B18	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40288	2841	0x0B19	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40289	2842	0x0B1A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40290	2843	0x0B1B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
40291	2844	0x0B1C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40292	2845	0x0B1D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40293	2846	0x0B1E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40294	2847	0x0B1F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40295	2848	0x0B20	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
40296	2849	0x0B21	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
40297	2850	0x0B22	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 2										
40299	2854	0x0B26	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40300	2855	0x0B27	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40301	2856	0x0B28	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40302	2857	0x0B29	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40303	2858	0x0B2A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40304	2859	0x0B2B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	

402861	2860	0x0B2C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402862	2861	0x0B2D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
402863	2862	0x0B2E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402864	2863	0x0B2F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
402865	2864	0x0B30	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
402866	2865	0x0B31	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
402867	2866	0x0B32	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 3										
402871	2870	0x0B36	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
402872	2871	0x0B37	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402873	2872	0x0B38	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
402874	2873	0x0B39	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402875	2874	0x0B3A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
402876	2875	0x0B3B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
402877	2876	0x0B3C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402878	2877	0x0B3D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
402879	2878	0x0B3E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402880	2879	0x0B3F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
402881	2880	0x0B40	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
402882	2881	0x0B41	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
402883	2882	0x0B42	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 4										
402887	2886	0x0B46	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	

402888	2887	0x0B47	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402889	2888	0x0B48	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
402890	2889	0x0B49	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402891	2890	0x0B4A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
402892	2891	0x0B4B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
402893	2892	0x0B4C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402894	2893	0x0B4D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
402895	2894	0x0B4E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402896	2895	0x0B4F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
402897	2896	0x0B50	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
402898	2897	0x0B51	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
402899	2898	0x0B52	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 5										
402903	2902	0x0B56	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
402904	2903	0x0B57	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402905	2904	0x0B58	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
402906	2905	0x0B59	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402907	2906	0x0B5A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
402908	2907	0x0B5B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
402909	2908	0x0B5C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402910	2909	0x0B5D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
402911	2910	0x0B5E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
402912	2911	0x0B5F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	

40291	2912	0x0B60	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
40291	2913	0x0B61	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
40291	2914	0x0B62	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 6										
40291	2918	0x0B66	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40292	2919	0x0B67	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40292	2920	0x0B68	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40292	2921	0x0B69	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40292	2922	0x0B6A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40292	2923	0x0B6B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
40292	2924	0x0B6C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40292	2925	0x0B6D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40292	2926	0x0B6E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40292	2927	0x0B6F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40292	2928	0x0B70	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
40293	2929	0x0B71	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
40293	2930	0x0B72	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 7										
40293	2934	0x0B76	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40293	2935	0x0B77	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40293	2936	0x0B78	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40293	2937	0x0B79	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40293	2938	0x0B7A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	

40294	2939	0x0B7B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
40295	2940	0x0B7C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40296	2941	0x0B7D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40297	2942	0x0B7E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40298	2943	0x0B7F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40299	2944	0x0B80	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
40300	2945	0x0B81	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
40301	2946	0x0B82	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 8										
40302	2950	0x0B86	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40303	2951	0x0B87	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40304	2952	0x0B88	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40305	2953	0x0B89	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40306	2954	0x0B8A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40307	2955	0x0B8B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
40308	2956	0x0B8C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40309	2957	0x0B8D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40310	2958	0x0B8E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40311	2959	0x0B8F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40312	2960	0x0B90	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
40313	2961	0x0B91	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
40314	2962	0x0B92	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 9										

40296	2966	0x0B96	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40296	2967	0x0B97	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40296	2968	0x0B98	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40297	2969	0x0B99	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40297	2970	0x0B9A	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40297	2971	0x0B9B	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
40297	2972	0x0B9C	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40297	2973	0x0B9D	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40297	2974	0x0B9E	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40297	2975	0x0B9F	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40297	2976	0x0BA0	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
40297	2977	0x0BA1	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
40297	2978	0x0BA2	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Conditional Logic Block 10										
40298	2982	0x0BA6	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40298	2983	0x0BA7	1	Condition 1 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40298	2984	0x0BA8	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40298	2985	0x0BA9	1	Condition 1 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40298	2986	0x0BAA	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
40298	2987	0x0BAB	1	Condition 1 Operator	Byte	0 - ==	0…9	N/A	R/W	
40298	2988	0x0BAC	1	Condition 2 Argument 1 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40298	2989	0x0BAD	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
40299	2990	0x0BAE	1	Condition 2 Argument 2 Source	Byte	0 - Control Not Used	0…17	N/A	R/W	

402992	2991	0x0BAF	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
402993	2992	0x0BB0	1	Condition 2 Operator	Byte	0 - ==	0…9	N/A	R/W	
402994	2993	0x0BB1	1	Conditional Result Operator	Byte	0 - OR	0…2	N/A	R/W	
402995	2994	0x0BB2	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Set-Reset Latch Function Block #1										
402999	2998	0x0BB6	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403000	2999	0x0BB7	1	Reset Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403001	3000	0x0BB8	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
403002	3001	0x0BB9	2	Reset Minimum Treshold	Float	0	0…100%	N/A	R/W	
403004	3003	0x0BBB	2	Reset Maximum Treshold	Float	100	0…100%	N/A	R/W	
403006	3005	0x0BBD	1	Set Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403007	3006	0x0BBE	1	Set number	Byte	1	Depends on control source	N/A	R/W	
403008	3007	0x0BBF	2	Set Maximum Treshold	Byte	0	0…100	N/A	R/W	
403010	3009	0x0BC1	2	Set Minimum Treshold	Byte	100	0…100	N/A	R/W	
403012	3011	0x0BC3	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Set-Reset Latch Function Block #2										
403020	3019	0x0BCB	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403021	3020	0x0BCC	1	Reset Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403022	3021	0x0BCD	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
403023	3022	0x0BCE	2	Reset Minimum Treshold	Float	0	0…100%	N/A	R/W	
403025	3024	0x0BD0	2	Reset Maximum Treshold	Float	100	0…100%	N/A	R/W	
403027	3026	0x0BD2	1	Set Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403028	3027	0x0BD3	1	Set number	Byte	1	Depends on control source	N/A	R/W	

40302 9	3028	0x0BD4	2	Set Maximum Treshold	Byte	0	0…100	N/A	R/W	
40303 1	3030	0x0BD6	2	Set Minimum Treshold	Byte	100	0…100	N/A	R/W	
40303 3	3032	0x0BD8	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Set-Reset Latch Function Block #3										
40304 1	3040	0x0BE0	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40304 2	3041	0x0BE1	1	Reset Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40304 3	3042	0x0BE2	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
40304 4	3043	0x0BE3	2	Reset Minimum Treshold	Float	0	0…100%	N/A	R/W	
40304 6	3045	0x0BE5	2	Reset Maximum Treshold	Float	100	0…100%	N/A	R/W	
40304 8	3047	0x0BE7	1	Set Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40304 9	3048	0x0BE8	1	Set number	Byte	1	Depends on control source	N/A	R/W	
40305 0	3049	0x0BE9	2	Set Maximum Treshold	Byte	0	0…100	N/A	R/W	
40305 2	3051	0x0BEB	2	Set Minimum Treshold	Byte	100	0…100	N/A	R/W	
40305 4	3053	0x0BED	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Set-Reset Latch Function Block #4										
40306 2	3061	0x0BF5	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
40306 3	3062	0x0BF6	1	Reset Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40306 4	3063	0x0BF7	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
40306 5	3064	0x0BF8	2	Reset Minimum Treshold	Float	0	0…100%	N/A	R/W	
40306 7	3066	0x0BFA	2	Reset Maximum Treshold	Float	100	0…100%	N/A	R/W	
40306 9	3068	0x0BFC	1	Set Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40307 0	3069	0x0BFD	1	Set number	Byte	1	Depends on control source	N/A	R/W	
40307 1	3070	0x0BFE	2	Set Maximum Treshold	Byte	0	0…100	N/A	R/W	

403073	3072	0x0C00	2	Set Minimum Treshold	Byte	100	0…100	N/A	R/W	
403075	3074	0x0C02	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Set-Reset Latch Function Block #5										
403083	3082	0x0C0A	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403084	3083	0x0C0B	1	Reset Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403085	3084	0x0C0C	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
403086	3085	0x0C0D	2	Reset Minimum Treshold	Float	0	0…100%	N/A	R/W	
403088	3087	0x0C0F	2	Reset Maximum Treshold	Float	100	0…100%	N/A	R/W	
403090	3089	0x0C11	1	Set Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403091	3090	0x0C12	1	Set number	Byte	1	Depends on control source	N/A	R/W	
403092	3091	0x0C13	2	Set Maximum Treshold	Byte	0	0…100	N/A	R/W	
403094	3093	0x0C15	2	Set Minimum Treshold	Byte	100	0…100	N/A	R/W	
403096	3095	0x0C17	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 1										
403104	3103	0x0C1F	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403105	3104	0x0C20	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
403106	3105	0x0C21	1	X-Axis Type	Byte			N/A	R/W	
403107	3106	0x0C22	1	Auto Repeat	Byte			N/A	R/W	
403108	3107	0x0C23	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
403109	3108	0x0C24	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
403110	3109	0x0C25	1	Response 1	Byte			N/A	R/W	
403111	3110	0x0C26	1	Response 2	Byte			N/A	R/W	
403112	3111	0x0C27	1	Response 3	Byte			N/A	R/W	
403113	3112	0x0C28	1	Response 4	Byte			N/A	R/W	

40314	3113	0x0C29	1	Response 5	Byte			N/A	R/W	
40315	3114	0x0C2A	1	Response 6	Byte			N/A	R/W	
40316	3115	0x0C2B	1	Response 7	Byte			N/A	R/W	
40317	3116	0x0C2C	1	Response 8	Byte			N/A	R/W	
40318	3117	0x0C2D	1	Response 9	Byte			N/A	R/W	
40319	3118	0x0C2E	1	Response 10	Byte			N/A	R/W	
40320	3119	0x0C2F	2	Point X1	Float			N/A	R/W	
40322	3121	0x0C31	2	Point X2	Float			N/A	R/W	
40324	3123	0x0C33	2	Point X3	Float			N/A	R/W	
40326	3125	0x0C35	2	Point X4	Float			N/A	R/W	
40328	3127	0x0C37	2	Point X5	Float			N/A	R/W	
40330	3129	0x0C39	2	Point X6	Float			N/A	R/W	
40332	3131	0x0C3B	2	Point X7	Float			N/A	R/W	
40334	3133	0x0C3D	2	Point X8	Float			N/A	R/W	
40336	3135	0x0C3F	2	Point X9	Float			N/A	R/W	
40338	3137	0x0C41	2	Point X10	Float			N/A	R/W	
40340	3139	0x0C43	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
40342	3141	0x0C45	2	Point Y2	Float		-10^6 to 10^6	N/A	R/W	
40344	3143	0x0C47	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
40346	3145	0x0C49	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
40348	3147	0x0C4B	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
40350	3149	0x0C4D	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
40352	3151	0x0C4F	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
40354	3153	0x0C51	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
40356	3155	0x0C53	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
40358	3157	0x0C55	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
40360	3159	0x0C57	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.

Lookup Table 2										
403168	3167	0x0C5F	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403169	3168	0x0C60	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
403170	3169	0x0C61	1	X-Axis Type	Byte			N/A	R/W	
403171	3170	0x0C62	1	Auto Repeat	Byte			N/A	R/W	
403172	3171	0x0C63	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
403173	3172	0x0C64	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
403174	3173	0x0C65	1	Response 1	Byte			N/A	R/W	
403175	3174	0x0C66	1	Response 2	Byte			N/A	R/W	
403176	3175	0x0C67	1	Response 3	Byte			N/A	R/W	
403177	3176	0x0C68	1	Response 4	Byte			N/A	R/W	
403178	3177	0x0C69	1	Response 5	Byte			N/A	R/W	
403179	3178	0x0C6A	1	Response 6	Byte			N/A	R/W	
403180	3179	0x0C6B	1	Response 7	Byte			N/A	R/W	
403181	3180	0x0C6C	1	Response 8	Byte			N/A	R/W	
403182	3181	0x0C6D	1	Response 9	Byte			N/A	R/W	
403183	3182	0x0C6E	1	Response 10	Byte			N/A	R/W	
403184	3183	0x0C6F	2	Point X1	Float			N/A	R/W	
403186	3185	0x0C71	2	Point X2	Float			N/A	R/W	
403188	3187	0x0C73	2	Point X3	Float			N/A	R/W	
403190	3189	0x0C75	2	Point X4	Float			N/A	R/W	
403192	3191	0x0C77	2	Point X5	Float			N/A	R/W	
403194	3193	0x0C79	2	Point X6	Float			N/A	R/W	
403196	3195	0x0C7B	2	Point X7	Float			N/A	R/W	
403198	3197	0x0C7D	2	Point X8	Float			N/A	R/W	
403200	3199	0x0C7F	2	Point X9	Float			N/A	R/W	
403202	3201	0x0C81	2	Point X10	Float			N/A	R/W	
403204	3203	0x0C83	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	

403206	3205	0x0C85	2	Point Y2	Float		-10^6 to 10^6	N/A	R/W	
403208	3207	0x0C87	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
403210	3209	0x0C89	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
403212	3211	0x0C8B	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
403214	3213	0x0C8D	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
403216	3215	0x0C8F	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
403218	3217	0x0C91	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
403220	3219	0x0C93	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
403222	3221	0x0C95	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
403224	3223	0x0C97	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 3										
403232	3231	0x0C9F	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403233	3232	0x0CA0	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
403234	3233	0x0CA1	1	X-Axis Type	Byte			N/A	R/W	
403235	3234	0x0CA2	1	Auto Repeat	Byte			N/A	R/W	
403236	3235	0x0CA3	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
403237	3236	0x0CA4	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
403238	3237	0x0CA5	1	Response 1	Byte			N/A	R/W	
403239	3238	0x0CA6	1	Response 2	Byte			N/A	R/W	
403240	3239	0x0CA7	1	Response 3	Byte			N/A	R/W	
403241	3240	0x0CA8	1	Response 4	Byte			N/A	R/W	
403242	3241	0x0CA9	1	Response 5	Byte			N/A	R/W	
403243	3242	0x0CAA	1	Response 6	Byte			N/A	R/W	
403244	3243	0x0CAB	1	Response 7	Byte			N/A	R/W	
403245	3244	0x0CAC	1	Response 8	Byte			N/A	R/W	
403246	3245	0x0CAD	1	Response 9	Byte			N/A	R/W	

403247	3246	0x0CAE	1	Response 10	Byte			N/A	R/W	
403248	3247	0x0CAF	2	Point X1	Float			N/A	R/W	
403250	3249	0x0CB1	2	Point X2	Float			N/A	R/W	
403252	3251	0x0CB3	2	Point X3	Float			N/A	R/W	
403254	3253	0x0CB5	2	Point X4	Float			N/A	R/W	
403256	3255	0x0CB7	2	Point X5	Float			N/A	R/W	
403258	3257	0x0CB9	2	Point X6	Float			N/A	R/W	
403260	3259	0x0CBB	2	Point X7	Float			N/A	R/W	
403262	3261	0x0CBD	2	Point X8	Float			N/A	R/W	
403264	3263	0x0CBF	2	Point X9	Float			N/A	R/W	
403266	3265	0x0CC1	2	Point X10	Float			N/A	R/W	
403268	3267	0x0CC3	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
403270	3269	0x0CC5	2	Point Y2	Float		-10^6 to 105	N/A	R/W	
403272	3271	0x0CC7	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
403274	3273	0x0CC9	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
403276	3275	0x0CCB	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
403278	3277	0x0CCD	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
403280	3279	0x0CCF	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
403282	3281	0x0CD1	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
403284	3283	0x0CD3	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
403286	3285	0x0CD5	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
403288	3287	0x0CD7	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 4										
403296	3295	0x0CDF	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403297	3296	0x0CE0	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
403298	3297	0x0CE1	1	X-Axis Type	Byte			N/A	R/W	

403299	3298	0x0CE2	1	Auto Repeat	Byte			N/A	R/W	
403300	3299	0x0CE3	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
403301	3300	0x0CE4	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
403302	3301	0x0CE5	1	Response 1	Byte			N/A	R/W	
403303	3302	0x0CE6	1	Response 2	Byte			N/A	R/W	
403304	3303	0x0CE7	1	Response 3	Byte			N/A	R/W	
403305	3304	0x0CE8	1	Response 4	Byte			N/A	R/W	
403306	3305	0x0CE9	1	Response 5	Byte			N/A	R/W	
403307	3306	0x0CEA	1	Response 6	Byte			N/A	R/W	
403308	3307	0x0CEB	1	Response 7	Byte			N/A	R/W	
403309	3308	0x0CEC	1	Response 8	Byte			N/A	R/W	
403310	3309	0x0CED	1	Response 9	Byte			N/A	R/W	
403311	3310	0x0CEE	1	Response 10	Byte			N/A	R/W	
403312	3311	0x0CEF	2	Point X1	Float			N/A	R/W	
403314	3313	0x0CF1	2	Point X2	Float			N/A	R/W	
403316	3315	0x0CF3	2	Point X3	Float			N/A	R/W	
403318	3317	0x0CF5	2	Point X4	Float			N/A	R/W	
403320	3319	0x0CF7	2	Point X5	Float			N/A	R/W	
403322	3321	0x0CF9	2	Point X6	Float			N/A	R/W	
403324	3323	0x0CFB	2	Point X7	Float			N/A	R/W	
403326	3325	0x0CFD	2	Point X8	Float			N/A	R/W	
403328	3327	0x0CFF	2	Point X9	Float			N/A	R/W	
403330	3329	0x0D01	2	Point X10	Float			N/A	R/W	
403332	3331	0x0D03	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
403334	3333	0x0D05	2	Point Y2	Float		-10^6 to 105	N/A	R/W	
403336	3335	0x0D07	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
403338	3337	0x0D09	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
403340	3339	0x0D0B	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	

40334	3341	0x0D0D	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
40334	3343	0x0D0F	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
40334	3345	0x0D11	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
40334	3347	0x0D13	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
40334	3349	0x0D15	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
40334	3351	0x0D17	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 5										
40336	3359	0x0D1F	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40336	3360	0x0D20	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
40336	3361	0x0D21	1	X-Axis Type	Byte			N/A	R/W	
40336	3362	0x0D22	1	Auto Repeat	Byte			N/A	R/W	
40336	3363	0x0D23	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
40336	3364	0x0D24	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
40336	3365	0x0D25	1	Response 1	Byte			N/A	R/W	
40336	3366	0x0D26	1	Response 2	Byte			N/A	R/W	
40336	3367	0x0D27	1	Response 3	Byte			N/A	R/W	
40336	3368	0x0D28	1	Response 4	Byte			N/A	R/W	
40337	3369	0x0D29	1	Response 5	Byte			N/A	R/W	
40337	3370	0x0D2A	1	Response 6	Byte			N/A	R/W	
40337	3371	0x0D2B	1	Response 7	Byte			N/A	R/W	
40337	3372	0x0D2C	1	Response 8	Byte			N/A	R/W	
40337	3373	0x0D2D	1	Response 9	Byte			N/A	R/W	
40337	3374	0x0D2E	1	Response 10	Byte			N/A	R/W	
40337	3375	0x0D2F	2	Point X1	Float			N/A	R/W	
40337	3377	0x0D31	2	Point X2	Float			N/A	R/W	
40337	3379	0x0D33	2	Point X3	Float			N/A	R/W	

403382	3381	0x0D35	2	Point X4	Float			N/A	R/W	
403384	3383	0x0D37	2	Point X5	Float			N/A	R/W	
403386	3385	0x0D39	2	Point X6	Float			N/A	R/W	
403388	3387	0x0D3B	2	Point X7	Float			N/A	R/W	
403390	3389	0x0D3D	2	Point X8	Float			N/A	R/W	
403392	3391	0x0D3F	2	Point X9	Float			N/A	R/W	
403394	3393	0x0D41	2	Point X10	Float			N/A	R/W	
403396	3395	0x0D43	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
403398	3397	0x0D45	2	Point Y2	Float		-10^6 to 105	N/A	R/W	
403400	3399	0x0D47	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
403402	3401	0x0D49	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
403404	3403	0x0D4B	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
403406	3405	0x0D4D	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
403408	3407	0x0D4F	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
403410	3409	0x0D51	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
403412	3411	0x0D53	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
403414	3413	0x0D55	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
403416	3415	0x0D57	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 6										
403424	3423	0x0D5F	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403425	3424	0x0D60	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
403426	3425	0x0D61	1	X-Axis Type	Byte			N/A	R/W	
403427	3426	0x0D62	1	Auto Repeat	Byte			N/A	R/W	
403428	3427	0x0D63	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
403429	3428	0x0D64	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
403430	3429	0x0D65	1	Response 1	Byte			N/A	R/W	

40341	3430	0x0D66	1	Response 2	Byte			N/A	R/W	
40342	3431	0x0D67	1	Response 3	Byte			N/A	R/W	
40343	3432	0x0D68	1	Response 4	Byte			N/A	R/W	
40344	3433	0x0D69	1	Response 5	Byte			N/A	R/W	
40345	3434	0x0D6A	1	Response 6	Byte			N/A	R/W	
40346	3435	0x0D6B	1	Response 7	Byte			N/A	R/W	
40347	3436	0x0D6C	1	Response 8	Byte			N/A	R/W	
40348	3437	0x0D6D	1	Response 9	Byte			N/A	R/W	
40349	3438	0x0D6E	1	Response 10	Byte			N/A	R/W	
40340	3439	0x0D6F	2	Point X1	Float			N/A	R/W	
40342	3441	0x0D71	2	Point X2	Float			N/A	R/W	
40344	3443	0x0D73	2	Point X3	Float			N/A	R/W	
40346	3445	0x0D75	2	Point X4	Float			N/A	R/W	
40348	3447	0x0D77	2	Point X5	Float			N/A	R/W	
40350	3449	0x0D79	2	Point X6	Float			N/A	R/W	
40352	3451	0x0D7B	2	Point X7	Float			N/A	R/W	
40354	3453	0x0D7D	2	Point X8	Float			N/A	R/W	
40356	3455	0x0D7F	2	Point X9	Float			N/A	R/W	
40358	3457	0x0D81	2	Point X10	Float			N/A	R/W	
40360	3459	0x0D83	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
40362	3461	0x0D85	2	Point Y2	Float		-10^6 to 105	N/A	R/W	
40364	3463	0x0D87	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
40366	3465	0x0D89	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
40368	3467	0x0D8B	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
40370	3469	0x0D8D	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
40372	3471	0x0D8F	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
40374	3473	0x0D91	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
40376	3475	0x0D93	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	

403478	3477	0x0D95	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
403480	3479	0x0D97	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 7										
403488	3487	0x0D9F	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403489	3488	0x0DA0	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
403490	3489	0x0DA1	1	X-Axis Type	Byte			N/A	R/W	
403491	3490	0x0DA2	1	Auto Repeat	Byte			N/A	R/W	
403492	3491	0x0DA3	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
403493	3492	0x0DA4	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
403494	3493	0x0DA5	1	Response 1	Byte			N/A	R/W	
403495	3494	0x0DA6	1	Response 2	Byte			N/A	R/W	
403496	3495	0x0DA7	1	Response 3	Byte			N/A	R/W	
403497	3496	0x0DA8	1	Response 4	Byte			N/A	R/W	
403498	3497	0x0DA9	1	Response 5	Byte			N/A	R/W	
403499	3498	0x0DAA	1	Response 6	Byte			N/A	R/W	
403500	3499	0x0DAB	1	Response 7	Byte			N/A	R/W	
403501	3500	0x0DAC	1	Response 8	Byte			N/A	R/W	
403502	3501	0x0DAD	1	Response 9	Byte			N/A	R/W	
403503	3502	0x0DAE	1	Response 10	Byte			N/A	R/W	
403504	3503	0x0DAF	2	Point X1	Float			N/A	R/W	
403506	3505	0x0DB1	2	Point X2	Float			N/A	R/W	
403508	3507	0x0DB3	2	Point X3	Float			N/A	R/W	
403510	3509	0x0DB5	2	Point X4	Float			N/A	R/W	
403512	3511	0x0DB7	2	Point X5	Float			N/A	R/W	
403514	3513	0x0DB9	2	Point X6	Float			N/A	R/W	
403516	3515	0x0DBB	2	Point X7	Float			N/A	R/W	

40351	3517	0x0DBD	2	Point X8	Float			N/A	R/W	
40352	3519	0x0DBF	2	Point X9	Float			N/A	R/W	
40354	3521	0x0DC1	2	Point X10	Float			N/A	R/W	
40356	3523	0x0DC3	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
40358	3525	0x0DC5	2	Point Y2	Float		-10^6 to 105	N/A	R/W	
40360	3527	0x0DC7	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
40362	3529	0x0DC9	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
40364	3531	0x0DCB	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
40366	3533	0x0DCD	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
40368	3535	0x0DCF	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
40370	3537	0x0DD1	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
40372	3539	0x0DD3	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
40374	3541	0x0DD5	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
40376	3543	0x0DD7	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 8										
40378	3551	0x0DDF	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
40379	3552	0x0DE0	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
40380	3553	0x0DE1	1	X-Axis Type	Byte			N/A	R/W	
40381	3554	0x0DE2	1	Auto Repeat	Byte			N/A	R/W	
40382	3555	0x0DE3	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
40383	3556	0x0DE4	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
40384	3557	0x0DE5	1	Response 1	Byte			N/A	R/W	
40385	3558	0x0DE6	1	Response 2	Byte			N/A	R/W	
40386	3559	0x0DE7	1	Response 3	Byte			N/A	R/W	
40387	3560	0x0DE8	1	Response 4	Byte			N/A	R/W	
40388	3561	0x0DE9	1	Response 5	Byte			N/A	R/W	

403563	3562	0x0DEA	1	Response 6	Byte			N/A	R/W	
403564	3563	0x0DEB	1	Response 7	Byte			N/A	R/W	
403565	3564	0x0DEC	1	Response 8	Byte			N/A	R/W	
403566	3565	0x0DED	1	Response 9	Byte			N/A	R/W	
403567	3566	0x0DEE	1	Response 10	Byte			N/A	R/W	
403568	3567	0x0DEF	2	Point X1	Float			N/A	R/W	
403570	3569	0x0DF1	2	Point X2	Float			N/A	R/W	
403572	3571	0x0DF3	2	Point X3	Float			N/A	R/W	
403574	3573	0x0DF5	2	Point X4	Float			N/A	R/W	
403576	3575	0x0DF7	2	Point X5	Float			N/A	R/W	
403578	3577	0x0DF9	2	Point X6	Float			N/A	R/W	
403580	3579	0x0DFB	2	Point X7	Float			N/A	R/W	
403582	3581	0x0DFD	2	Point X8	Float			N/A	R/W	
403584	3583	0x0DFF	2	Point X9	Float			N/A	R/W	
403586	3585	0x0E01	2	Point X10	Float			N/A	R/W	
403588	3587	0x0E03	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
403590	3589	0x0E05	2	Point Y2	Float		-10^6 to 105	N/A	R/W	
403592	3591	0x0E07	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
403594	3593	0x0E09	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
403596	3595	0x0E0B	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
403598	3597	0x0E0D	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
403600	3599	0x0E0F	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
403602	3601	0x0E11	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
403604	3603	0x0E13	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
403606	3605	0x0E15	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
403608	3607	0x0E17	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 9										

403616	3615	0x0E1F	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403617	3616	0x0E20	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
403618	3617	0x0E21	1	X-Axis Type	Byte			N/A	R/W	
403619	3618	0x0E22	1	Auto Repeat	Byte			N/A	R/W	
403620	3619	0x0E23	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
403621	3620	0x0E24	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
403622	3621	0x0E25	1	Response 1	Byte			N/A	R/W	
403623	3622	0x0E26	1	Response 2	Byte			N/A	R/W	
403624	3623	0x0E27	1	Response 3	Byte			N/A	R/W	
403625	3624	0x0E28	1	Response 4	Byte			N/A	R/W	
403626	3625	0x0E29	1	Response 5	Byte			N/A	R/W	
403627	3626	0x0E2A	1	Response 6	Byte			N/A	R/W	
403628	3627	0x0E2B	1	Response 7	Byte			N/A	R/W	
403629	3628	0x0E2C	1	Response 8	Byte			N/A	R/W	
403630	3629	0x0E2D	1	Response 9	Byte			N/A	R/W	
403631	3630	0x0E2E	1	Response 10	Byte			N/A	R/W	
403632	3631	0x0E2F	2	Point X1	Float			N/A	R/W	
403634	3633	0x0E31	2	Point X2	Float			N/A	R/W	
403636	3635	0x0E33	2	Point X3	Float			N/A	R/W	
403638	3637	0x0E35	2	Point X4	Float			N/A	R/W	
403640	3639	0x0E37	2	Point X5	Float			N/A	R/W	
403642	3641	0x0E39	2	Point X6	Float			N/A	R/W	
403644	3643	0x0E3B	2	Point X7	Float			N/A	R/W	
403646	3645	0x0E3D	2	Point X8	Float			N/A	R/W	
403648	3647	0x0E3F	2	Point X9	Float			N/A	R/W	
403650	3649	0x0E41	2	Point X10	Float			N/A	R/W	
403652	3651	0x0E43	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
403654	3653	0x0E45	2	Point Y2	Float		-10^6 to 105	N/A	R/W	

403656	3655	0x0E47	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
403658	3657	0x0E49	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
403660	3659	0x0E4B	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
403662	3661	0x0E4D	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
403664	3663	0x0E4F	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
403666	3665	0x0E51	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
403668	3667	0x0E53	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
403670	3669	0x0E55	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
403672	3671	0x0E57	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lookup Table 10										
403680	3679	0x0E5F	1	X-Axis Source	Byte	0 - Control Not Used	0…17	N/A	R/W	
403681	3680	0x0E60	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
403682	3681	0x0E61	1	X-Axis Type	Byte			N/A	R/W	
403683	3682	0x0E62	1	Auto Repeat	Byte			N/A	R/W	
403684	3683	0x0E63	1	X Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects X points
403685	3684	0x0E64	1	Y Decimal Digits	Byte			N/A	R/W	Resolution is 10^x, affects Y points
403686	3685	0x0E65	1	Response 1	Byte			N/A	R/W	
403687	3686	0x0E66	1	Response 2	Byte			N/A	R/W	
403688	3687	0x0E67	1	Response 3	Byte			N/A	R/W	
403689	3688	0x0E68	1	Response 4	Byte			N/A	R/W	
403690	3689	0x0E69	1	Response 5	Byte			N/A	R/W	
403691	3690	0x0E6A	1	Response 6	Byte			N/A	R/W	
403692	3691	0x0E6B	1	Response 7	Byte			N/A	R/W	
403693	3692	0x0E6C	1	Response 8	Byte			N/A	R/W	
403694	3693	0x0E6D	1	Response 9	Byte			N/A	R/W	
403695	3694	0x0E6E	1	Response 10	Byte			N/A	R/W	

403696	3695	0x0E6F	2	Point X1	Float			N/A	R/W	
403698	3697	0x0E71	2	Point X2	Float			N/A	R/W	
403700	3699	0x0E73	2	Point X3	Float			N/A	R/W	
403702	3701	0x0E75	2	Point X4	Float			N/A	R/W	
403704	3703	0x0E77	2	Point X5	Float			N/A	R/W	
403706	3705	0x0E79	2	Point X6	Float			N/A	R/W	
403708	3707	0x0E7B	2	Point X7	Float			N/A	R/W	
403710	3709	0x0E7D	2	Point X8	Float			N/A	R/W	
403712	3711	0x0E7F	2	Point X9	Float			N/A	R/W	
403714	3713	0x0E81	2	Point X10	Float			N/A	R/W	
403716	3715	0x0E83	2	Point Y1	Float		-10^6 to 10^6	N/A	R/W	
403718	3717	0x0E85	2	Point Y2	Float		-10^6 to 105	N/A	R/W	
403720	3719	0x0E87	2	Point Y3	Float		-10^6 to 10^6	N/A	R/W	
403722	3721	0x0E89	2	Point Y4	Float		-10^6 to 10^6	N/A	R/W	
403724	3723	0x0E8B	2	Point Y5	Float		-10^6 to 10^6	N/A	R/W	
403726	3725	0x0E8D	2	Point Y6	Float		-10^6 to 10^6	N/A	R/W	
403728	3727	0x0E8F	2	Point Y7	Float		-10^6 to 10^6	N/A	R/W	
403730	3729	0x0E91	2	Point Y8	Float		-10^6 to 10^6	N/A	R/W	
403732	3731	0x0E93	2	Point Y9	Float		-10^6 to 10^6	N/A	R/W	
403734	3733	0x0E95	2	Point Y10	Float		-10^6 to 10^6	N/A	R/W	
403736	3735	0x0E97	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Programmable Logic 1										
403744	3743	0x0E9F	1	Logic Enabled	Byte	0 - No	No / Yes	N/A	R/W	See Table 16
403745	3744	0x0EA0	1	Table Number 1	Byte	1 - Lookup Table 1	Drop List	N/A	R/W	See Table 16
403746	3745	0x0EA1	1	Logical Operator 1	Byte	0	Drop List	N/A	R/W	See Table 14
403747	3746	0x0EA2	1	Table 1 - Condition 1 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16

403748	3747	0x0EA3	1	Table 1 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403749	3748	0x0EA4	1	Table 1 - Condition 1 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403750	3749	0x0EA5	1	Table 1 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403751	3750	0x0EA6	1	Table 1 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403752	3751	0x0EA7	1	Table 1 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403753	3752	0x0EA8	1	Table 1 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403754	3753	0x0EA9	1	Table 1 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403755	3754	0x0EAA	1	Table 1 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403756	3755	0x0EAB	1	Table 1 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403757	3756	0x0EAC	1	Table 1 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403758	3757	0x0EAD	1	Table 1 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403759	3758	0x0EAE	1	Table 1 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403760	3759	0x0EAF	1	Table 1 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403761	3760	0x0EB0	1	Table 1 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403762	3761	0x0EB1	1	Table Number 2	Byte	1 - Lookup Table 2	Drop List	N/A	R/W	See Table 16
403763	3762	0x0EB2	1	Logical Operator 2	Byte	1 - Cnd1 & Cnd2 & Cnd3	Drop List	N/A	R/W	See Table 14
403764	3763	0x0EB3	1	Table 2 - Condition 1 Argument 1 Source	Byte	1 - CAN Receve Message	Drop List	N/A	R/W	See Table 16
403765	3764	0x0EB4	1	Table 2 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403766	3765	0x0EB5	1	Table 2 - Condition 1 Argument 2 Source	Byte	3 - Constant Continuous Data	Drop List	N/A	R/W	See Table 16
403767	3766	0x0EB6	1	Table 2 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403768	3767	0x0EB7	1	Table 2 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13

403769	3768	0x0EB8	1	Table 2 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403770	3769	0x0EB9	1	Table 2 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403771	3770	0x0EBA	1	Table 2 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403772	3771	0x0EBB	1	Table 2 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403773	3772	0x0EBC	1	Table 2 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403774	3773	0x0EBD	1	Table 2 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403775	3774	0x0EBE	1	Table 2 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403776	3775	0x0EBF	1	Table 2 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403777	3776	0x0EC0	1	Table 2 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403778	3777	0x0EC1	1	Table 2 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403779	3778	0x0EC2	1	Table Number 3	Byte	0	Drop List	N/A	R/W	See Table 16
403780	3779	0x0EC3	1	Logical Operator 3	Byte	0	Drop List	N/A	R/W	See Table 14
403781	3780	0x0EC4	1	Table 3 - Condition 1 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403782	3781	0x0EC5	1	Table 3 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403783	3782	0x0EC6	1	Table 3 - Condition 1 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403784	3783	0x0EC7	1	Table 3 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403785	3784	0x0EC8	1	Table 3 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403786	3785	0x0EC9	1	Table 3 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403787	3786	0x0ECA	1	Table 3 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403788	3787	0x0ECB	1	Table 3 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403789	3788	0x0ECC	1	Table 3 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16

403790	3789	0x0ECD	1	Table 3 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403791	3790	0x0ECE	1	Table 3 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403792	3791	0x0ECF	1	Table 3 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403793	3792	0x0ED0	1	Table 3 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403794	3793	0x0ED1	1	Table 3 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403795	3794	0x0ED2	1	Table 3 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403796	3795	0x0ED3	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Programmable Logic 2										
403808	3807	0x0EDF	1	Logic Enabled	Byte	0 - No	No / Yes	N/A	R/W	See Table 16
403809	3808	0x0EE0	1	Table Number 1	Byte	1 - Lookup Table 1	Drop List	N/A	R/W	See Table 16
403810	3809	0x0EE1	1	Logical Operator 1	Byte	0	Drop List	N/A	R/W	See Table 14
403811	3810	0x0EE2	1	Table 1 - Condition 1 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403812	3811	0x0EE3	1	Table 1 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403813	3812	0x0EE4	1	Table 1 - Condition 1 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403814	3813	0x0EE5	1	Table 1 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403815	3814	0x0EE6	1	Table 1 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403816	3815	0x0EE7	1	Table 1 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403817	3816	0x0EE8	1	Table 1 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403818	3817	0x0EE9	1	Table 1 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403819	3818	0x0EEA	1	Table 1 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403820	3819	0x0EEB	1	Table 1 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13

403821	3820	0x0EEC	1	Table 1 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403822	3821	0x0EED	1	Table 1 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403823	3822	0x0EEE	1	Table 1 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403824	3823	0x0EEF	1	Table 1 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403825	3824	0x0EF0	1	Table 1 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403826	3825	0x0EF1	1	Table Number 2	Byte	1 - Lookup Table 2	Drop List	N/A	R/W	See Table 16
403827	3826	0x0EF2	1	Logical Operator 2	Byte	1 - Cnd1 & Cnd2 & Cnd3	Drop List	N/A	R/W	See Table 14
403828	3827	0x0EF3	1	Table 2 - Condition 1 Argument 1 Source	Byte	1 - CAN Receve Message	Drop List	N/A	R/W	See Table 16
403829	3828	0x0EF4	1	Table 2 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403830	3829	0x0EF5	1	Table 2 - Condition 1 Argument 2 Source	Byte	3 - Constant Continuous Data	Drop List	N/A	R/W	See Table 16
403831	3830	0x0EF6	1	Table 2 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403832	3831	0x0EF7	1	Table 2 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403833	3832	0x0EF8	1	Table 2 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403834	3833	0x0EF9	1	Table 2 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403835	3834	0x0EFA	1	Table 2 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403836	3835	0x0EFB	1	Table 2 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403837	3836	0x0EFC	1	Table 2 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403838	3837	0x0EFD	1	Table 2 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403839	3838	0x0EFE	1	Table 2 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403840	3839	0x0EFF	1	Table 2 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403841	3840	0x0F00	1	Table 2 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16

403842	3841	0x0F01	1	Table 2 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403843	3842	0x0F02	1	Table Number 3	Byte	0	Drop List	N/A	R/W	See Table 16
403844	3843	0x0F03	1	Logical Operator 3	Byte	0	Drop List	N/A	R/W	See Table 14
403845	3844	0x0F04	1	Table 3 - Condition 1 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403846	3845	0x0F05	1	Table 3 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403847	3846	0x0F06	1	Table 3 - Condition 1 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403848	3847	0x0F07	1	Table 3 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403849	3848	0x0F08	1	Table 3 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403850	3849	0x0F09	1	Table 3 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403851	3850	0x0F0A	1	Table 3 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403852	3851	0x0F0B	1	Table 3 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403853	3852	0x0F0C	1	Table 3 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403854	3853	0x0F0D	1	Table 3 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403855	3854	0x0F0E	1	Table 3 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403856	3855	0x0F0F	1	Table 3 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403857	3856	0x0F10	1	Table 3 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403858	3857	0x0F11	1	Table 3 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403859	3858	0x0F12	1	Table 3 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403860	3859	0x0F13	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Programmable Logic 3										
403872	3871	0x0F1F	1	Logic Enabled	Byte	0 - No	No / Yes	N/A	R/W	See Table 16

403873	3872	0x0F20	1	Table Number 1	Byte	1 - Lookup Table 1	Drop List	N/A	R/W	See Table 16
403874	3873	0x0F21	1	Logical Operator 1	Byte	0	Drop List	N/A	R/W	See Table 14
403875	3874	0x0F22	1	Table 1 - Condition 1 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403876	3875	0x0F23	1	Table 1 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403877	3876	0x0F24	1	Table 1 - Condition 1 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403878	3877	0x0F25	1	Table 1 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403879	3878	0x0F26	1	Table 1 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403880	3879	0x0F27	1	Table 1 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403881	3880	0x0F28	1	Table 1 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403882	3881	0x0F29	1	Table 1 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403883	3882	0x0F2A	1	Table 1 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403884	3883	0x0F2B	1	Table 1 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403885	3884	0x0F2C	1	Table 1 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403886	3885	0x0F2D	1	Table 1 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403887	3886	0x0F2E	1	Table 1 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403888	3887	0x0F2F	1	Table 1 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403889	3888	0x0F30	1	Table 1 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403890	3889	0x0F31	1	Table Number 2	Byte	1 - Lookup Table 2	Drop List	N/A	R/W	See Table 16
403891	3890	0x0F32	1	Logical Operator 2	Byte	1 - Cnd1 & Cnd2 & Cnd3	Drop List	N/A	R/W	See Table 14
403892	3891	0x0F33	1	Table 2 - Condition 1 Argument 1 Source	Byte	1 - CAN Receve Message	Drop List	N/A	R/W	See Table 16
403893	3892	0x0F34	1	Table 2 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16

403894	3893	0x0F35	1	Table 2 - Condition 1 Argument 2 Source	Byte	3 - Constant Continuous Data	Drop List	N/A	R/W	See Table 16
403895	3894	0x0F36	1	Table 2 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403896	3895	0x0F37	1	Table 2 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403897	3896	0x0F38	1	Table 2 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403898	3897	0x0F39	1	Table 2 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403899	3898	0x0F3A	1	Table 2 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403900	3899	0x0F3B	1	Table 2 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403901	3900	0x0F3C	1	Table 2 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403902	3901	0x0F3D	1	Table 2 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403903	3902	0x0F3E	1	Table 2 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403904	3903	0x0F3F	1	Table 2 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403905	3904	0x0F40	1	Table 2 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403906	3905	0x0F41	1	Table 2 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403907	3906	0x0F42	1	Table Number 3	Byte	0	Drop List	N/A	R/W	See Table 16
403908	3907	0x0F43	1	Logical Operator 3	Byte	0	Drop List	N/A	R/W	See Table 14
403909	3908	0x0F44	1	Table 3 - Condition 1 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403910	3909	0x0F45	1	Table 3 - Condition 1 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403911	3910	0x0F46	1	Table 3 - Condition 1 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403912	3911	0x0F47	1	Table 3 - Condition 1 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403913	3912	0x0F48	1	Table 3 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403914	3913	0x0F49	1	Table 3 - Condition 2 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16

403915	3914	0x0F4A	1	Table 3 - Condition 2 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403916	3915	0x0F4B	1	Table 3 - Condition 2 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403917	3916	0x0F4C	1	Table 3 - Condition 2 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403918	3917	0x0F4D	1	Table 3 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403919	3918	0x0F4E	1	Table 3 - Condition 3 Argument 1 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403920	3919	0x0F4F	1	Table 3 - Condition 3 Argument 1 Number	Byte	1		N/A	R/W	See Table 16
403921	3920	0x0F50	1	Table 3 - Condition 3 Argument 2 Source	Byte	0	Drop List	N/A	R/W	See Table 16
403922	3921	0x0F51	1	Table 3 - Condition 3 Argument 2 Number	Byte	1		N/A	R/W	See Table 16
403923	3922	0x0F52	1	Table 3 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	See Table 13
403924	3923	0x0F53	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Miscellaneous Inputs										
403936	3935	0x0F5F	2	Undervoltage Threshold	Float	10V	6V…	V	R/W	
403938	3937	0x0F61	2	Overvoltage Threshold	Float	35V	…36V	V	R/W	
403940	3939	0x0F63	2	Shutdown Temperature	Float	75°C	40…85°C	Deg.C	R/W	
403942	3941	0x0F65	1	CAN1 Diagnostic Messages Settings	Byte	0	0…2	N/A	R/W	
403943	3942	0x0F66	1	CAN2 Diagnostic Messages Settings	Byte	0	0…2	N/A	R/W	
403943	3942	0x0F66	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Power Undervoltage Fault										
403952	3951	0x0F6F	1	Event Generates a DTC in DM1	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403953	3952	0x0F70	1	Event Only Cleared by DM11	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403954	3953	0x0F71	1	Lamp Set by Event in DM1	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403955	3954	0x0F72	2	SPN for Event used in DTC	Dword	0	0…524287	N/A	R/W	Refer to Section 1.2

403957	3956	0x0F74	1	FMI for Event used in DTC	Byte	0		N/A	R/W	Refer to Section 1.2
403958	3957	0x0F75	1	Delay Before Sending DM1	Word	100 ms	0…60000 ms	ms	R/W	Refer to Section 1.2
403959	3958	0x0F76	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Power Overvoltage Fault										
403968	3967	0x0F7F	1	Event Generates a DTC in DM1	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403969	3968	0x0F80	1	Event Only Cleared by DM11	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403970	3969	0x0F81	1	Lamp Set by Event in DM1	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403971	3970	0x0F82	2	SPN for Event used in DTC	Dword	0	0…524287	N/A	R/W	Refer to Section 1.2
403973	3972	0x0F84	1	FMI for Event used in DTC	Byte	0		N/A	R/W	Refer to Section 1.2
403974	3973	0x0F85	1	Delay Before Sending DM1	Word	100 ms	0…60000 ms	ms	R/W	Refer to Section 1.2
403975	3974	0x0F86	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Over Temperature Fault										
403984	3983	0x0F8F	1	Event Generates a DTC in DM1	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403985	3984	0x0F90	1	Event Only Cleared by DM11	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403986	3985	0x0F91	1	Lamp Set by Event in DM1	Byte	FALSE		N/A	R/W	Refer to Section 1.2
403987	3986	0x0F92	2	SPN for Event used in DTC	Dword	0	0…524287	N/A	R/W	Refer to Section 1.2
403989	3988	0x0F94	1	FMI for Event used in DTC	Byte	0		N/A	R/W	Refer to Section 1.2
403990	3989	0x0F95	1	Delay Before Sending DM1	Word	100 ms	0…60000 ms	ms	R/W	Refer to Section 1.2
403991	3990	0x0F96	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Lost Communication										
404000	3999	0x0F9F	1	Event Generates a DTC in DM1	Byte	FALSE		N/A	R/W	Refer to Section 1.2
404001	4000	0x0FA0	1	Event Only Cleared by DM11	Byte	FALSE		N/A	R/W	Refer to Section 1.2
404002	4001	0x0FA1	1	Lamp Set by Event in DM1	Byte	FALSE		N/A	R/W	Refer to Section 1.2

404003	4002	0x0FA2	2	SPN for Event used in DTC	Dword	0	0…524287	N/A	R/W	Refer to Section 1.2
404005	4004	0x0FA4	1	FMI for Event used in DTC	Byte	0		N/A	R/W	Refer to Section 1.2
404006	4005	0x0FA5	1	Delay Before Sending DM1	Word	100 ms	0…60000 ms	ms	R/W	Refer to Section 1.2
404007	4006	0x0FA6	9	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
Constant Data										
404016	4015	0x0FAF	2	Global Continuous Constant Signal	Float	0	Any value	N/A	R/W	Output signal value of the Global Continuous Constant Signal
404018	4017	0x0FB1	2	Global Discrete Constant Signal	DWord	0	Any value [0… 4294967295 (0xFFFFFFFF)]	N/A	R/W	Output signal value of the Global Discrete Constant Signal
404020	4019	0x0FB3	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
J1939 Network										
404032	4031	0x0400	1	ECU Instance Number	Byte	0 - Instance #1	0…7 0 - Instance #1, … 7 - Instance #8	N/A	R/W	ECU Instance field of the J1939 ECU Name
404033	4032	0x0FC0	1	ECU Address	Byte	128	0…253	N/A	R/W	J1939 ECU address
404034	4033	0x0FC1	14	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
Ethernet										
404048	4047	0x0FCF	3	MAC Address	Byte[6]	Set at the factory	Any valid MAC address	N/A	RO	Ethernet MAC Address. Set at the factory. Writing is allowed but does not change the value.
404051	4050	0x0FD2	2	IP Address	Byte[4]	192.168.0.34	Any IP address	N/A	R/W	The device IP address
404053	4052	0x0FD4	2	Subnet Mask	Byte[4]	255.255.255.0	Any IP address	N/A	R/W	The device subnet mask
404055	4054	0x0FD6	2	Gateway	Byte[4]	192.168.0.1	Any IP address	N/A	R/W	The device default gateway

40457	4056	0x0FD8	1	Modbus Port	Word	502	Any port value except the Discovery Port (35100)	N/A	R/W	The Modbus listening port
40458	4057	0x0FD9	1	Modbus Timeout	Word	1000	1…10000	ms	R/W	The Modbus communication timeout
40459	4058	0x0FDA	6	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
CAN Receive 1										
40464	4063	0x0FDF	1	CAN Interface	Byte	1 - CAN 1	0 - Undefined, 1 - CAN 1, 2 - CAN 2, 3 - Both Interfaces	N/A	R/W	CAN input signal type
40465	4064	0x0FE0	1	Signal Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	CAN input signal type
40466	4065	0x0FE1	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
40468	4067	0x0FE3	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will be accepted, if "Yes".
40469	4068	0x0FE4	1	Selected Address	Byte	0	0…253	N/A	R/W	Address of the ECU transmitting CAN messages if PGN From Selected Address is set to "Yes".
40470	4069	0x0FE5	1	Data Position Byte	Byte	1	0…8	N/A	R/W	Start byte of the CAN input signal in the CAN message data frame
40471	4070	0x0FE6	1	Data Position Bit	Byte	1	0…8	N/A	R/W	Start bit of the CAN input signal in the Data Position Byte
40472	4071	0x0FE7	1	Size	Byte	1	0…32	N/A	R/W	CAN input signal size
40473	4072	0x0FE8	2	Resolution	Float	1	Any value	signal units / bit	R/W	CAN input signal resolution for continuous input signals.
40475	4074	0x0FEA	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals.
40477	4076	0x0FEC	1	Autoreset Time	Word	500	0…10000	ms	R/W	Function block signal output auto-reset time. If Autoreset Time is 0, the auto-reset is disabled.

40407 8	4077	0x0FED	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
CAN Receive 2										
40485	4084	0x0FF4	1	CAN Interface	Byte	1 - CAN 1	0 - Undefined, 1 - CAN 1, 2 - CAN 2, 3 - Both Interfaces	N/A	R/W	CAN input signal type
40486	4085	0x0FF5	1	Signal Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	CAN input signal type
40487	4086	0x0FF6	2	PGN	Word	65535	Any J1939 PGN value	N/A	R/W	Signal message PGN value
40489	4088	0x0FF8	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will be accepted, if "Yes".
40490	4089	0x0FF9	1	Selected Address	Byte	0	0…253	N/A	R/W	Address of the ECU transmitting CAN messages if PGN From Selected Address is set to "Yes".
40491	4090	0x0FFA	1	Data Position Byte	Byte	1	0…8	N/A	R/W	Start byte of the CAN input signal in the CAN message data frame
40492	4091	0x0FFB	1	Data Position Bit	Byte	1	0…8	N/A	R/W	Start bit of the CAN input signal in the Data Position Byte
40493	4092	0x0FFC	1	Size	Byte	1	0…32	N/A	R/W	CAN input signal size
40494	4093	0x0FFD	2	Resolution	Float	1	Any value	signal units / bit	R/W	CAN input signal resolution for continuous input signals.
40496	4095	0x0FFF	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals.
40498	4097	0x1001	1	Autoreset Time	Word	500	0…10000	ms	R/W	Function block signal output auto-reset time. If Autoreset Time is 0, the auto-reset is disabled.
40499	4098	0x1002	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
CAN Receive 3										

404106	4105	0x1009	1	CAN Interface	Byte	1 - CAN 1	0 - Undefined, 1 - CAN 1, 2 - CAN 2, 3 - Both Interfaces	N/A	R/W	Defines the CAN interface used to receive messages (CAN 1, CAN 2 or Both)
404107	4106	0x100A	1	Signal Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	CAN input signal type
404108	4107	0x100B	2	PGN	Word	65535	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404110	4109	0x100D	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will be accepted, if "Yes".
404111	4110	0x100E	1	Selected Address	Byte	0	0…253	N/A	R/W	Address of the ECU transmitting CAN messages if PGN From Selected Address is set to "Yes".
404112	4111	0x100F	1	Data Position Byte	Byte	1	0…8	N/A	R/W	Start byte of the CAN input signal in the CAN message data frame
404113	4112	0x1010	1	Data Position Bit	Byte	1	0…8	N/A	R/W	Start bit of the CAN input signal in the Data Position Byte
404114	4113	0x1011	1	Size	Byte	1	0…32	bit	R/W	CAN input signal size
404115	4114	0x1012	2	Resolution	Float	1	Any value	signal units / bit	R/W	CAN input signal resolution for continuous input signals.
404117	4116	0x1014	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals.
404119	4118	0x1016	1	Autoreset Time	Word	500	0…10000	ms	R/W	Function block signal output auto-reset time. If Autoreset Time is 0, the auto-reset is disabled.
404120	4119	0x1017	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
CAN Transmit 1										
404127	4126	0x101E	1	CAN Interface	Byte	1 - CAN 1	0 - Undefined, 1 - CAN 1, 2 - CAN 2, 3 - Both Interfaces	N/A	R/W	Defines the CAN interface used to send messages (CAN 1, CAN 2 or Both)

404128	4127	0x101F	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	CAN message PGN
404130	4129	0x1021	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Transmission Enable. Enables the CAN output message transmission
404131	4130	0x1022	1	Transmission Rate	Word	0	0…10000	ms	R/W	CAN output message transmission rate. If 0 – transmission is upon request.
404132	4131	0x1023	1	Destination Address	Byte	255	0…255	N/A	R/W	Destination address of the PDU1 PGN messages
404133	4132	0x1024	1	Length	Byte	8	0…8	byte	R/W	CAN message data frame length
404134	4133	0x1025	1	Priority	Byte	6	0…7	N/A	R/W	CAN message priority
404135	4134	0x1026	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 1-st CAN output signal
404136	4135	0x1027	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
404137	4136	0x1028	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
404138	4137	0x1029	1	Signal #1 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 1-st CAN output signal
404139	4138	0x102A	1	Signal #1 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 1-st CAN output signal
404140	4139	0x102B	1	Signal #1 Size	Byte	1	0…32	bit	R/W	Size of the 1-st CAN output signal
404141	4140	0x102C	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
404143	4142	0x102E	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
404145	4144	0x1030	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 2-nd CAN output signal
404146	4145	0x1031	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
404147	4146	0x1032	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
404148	4147	0x1033	1	Signal #2 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 2-nd CAN output signal
404149	4148	0x1034	1	Signal #2 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 2-nd CAN output signal

404150	4149	0x1035	1	Signal #2 Size	Byte	1	0…32	bit	R/W	Size of the 2-nd CAN output signal
404151	4150	0x1036	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
404153	4152	0x1038	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
404155	4154	0x103A	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 3-rd CAN output signal
404156	4155	0x103B	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
404157	4156	0x103C	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
404158	4157	0x103D	1	Signal #3 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 3-rd CAN output signal
404159	4158	0x103E	1	Signal #3 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 3-rd CAN output signal
404160	4159	0x103F	1	Signal #3 Size	Byte	1	0…32	bit	R/W	Size of the 3-rd CAN output signal
404161	4160	0x1040	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
404163	4162	0x1042	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
404165	4164	0x1044	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404166	4165	0x1045	1	Signal #4 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404167	4166	0x1046	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
404168	4167	0x1047	1	Signal #4 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404169	4168	0x1048	1	Signal #4 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404170	4169	0x1049	1	Signal #4 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404171	4170	0x104A	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404173	4172	0x104C	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal

404175	4174	0x104E	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404176	4175	0x104F	1	Signal #5 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404177	4176	0x1050	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
404178	4177	0x1051	1	Signal #5 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404179	4178	0x1052	1	Signal #5 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404180	4179	0x1053	1	Signal #5 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404181	4180	0x1054	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404183	4182	0x1056	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404185	4184	0x1058	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404186	4185	0x1059	1	Signal #6 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404187	4186	0x105A	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
404188	4187	0x105B	1	Signal #6 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404189	4188	0x105C	1	Signal #6 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404190	4189	0x105D	1	Signal #6 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404191	4190	0x105E	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404193	4192	0x1060	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404195	4194	0x1062	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404196	4195	0x1063	1	Signal #7 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal

404197	4196	0x1064	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
404198	4197	0x1065	1	Signal #7 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404199	4198	0x1066	1	Signal #7 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404200	4199	0x1067	1	Signal #7 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404201	4200	0x1068	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404203	4202	0x106A	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404205	4204	0x106C	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404206	4205	0x106D	1	Signal #8 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404207	4206	0x106E	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
404208	4207	0x106F	1	Signal #8 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404209	4208	0x1070	1	Signal #8 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404210	4209	0x1071	1	Signal #8 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404211	4210	0x1072	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404213	4212	0x1074	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404215	4214	0x1076	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404216	4215	0x1077	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404217	4216	0x1078	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
404218	4217	0x1079	1	Signal #9 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404219	4218	0x107A	1	Signal #9 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404220	4219	0x107B	1	Signal #9 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal

40421	4220	0x107C	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
40423	4222	0x107E	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
40425	4224	0x1080	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
40426	4225	0x1081	1	Signal #10 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
40427	4226	0x1082	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
40428	4227	0x1083	1	Signal #10 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
40429	4228	0x1084	1	Signal #10 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
40430	4229	0x1085	1	Signal #10 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
40431	4230	0x1086	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
40433	4232	0x1088	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
40435	4234	0x108A	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
CAN Transmit 2										
40439	4238	0x108E	1	CAN Interface	Byte	1 - CAN 1	0 - Undefined, 1 - CAN 1, 2 - CAN 2, 3 - Both Interfaces	N/A	R/W	Defines the CAN interface used to send messages (CAN 1, CAN 2 or Both)
40440	4239	0x108F	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	CAN message PGN
40442	4241	0x1091	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Transmission Enable. Enables the CAN output message transmission
40443	4242	0x1092	1	Transmission Rate	Word	0	0…10000	ms	R/W	CAN output message transmission rate. If 0 – transmission is upon request.
40444	4243	0x1093	1	Destination Address	Byte	255	0…255	N/A	R/W	Destination address of the PDU1 PGN messages

404245	4244	0x1094	1	Length	Byte	8	0…8	byte	R/W	CAN message data frame length
404246	4245	0x1095	1	Priority	Byte	6	0…7	N/A	R/W	CAN message priority
404247	4246	0x1096	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 1-st CAN output signal
404248	4247	0x1097	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
404249	4248	0x1098	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
404250	4249	0x1099	1	Signal #1 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 1-st CAN output signal
404251	4250	0x109A	1	Signal #1 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 1-st CAN output signal
404252	4251	0x109B	1	Signal #1 Size	Byte	1	0…32	bit	R/W	Size of the 1-st CAN output signal
404253	4252	0x109C	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
404255	4254	0x109E	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
404257	4256	0x10A0	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 2-nd CAN output signal
404258	4257	0x10A1	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
404259	4258	0x10A2	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
404260	4259	0x10A3	1	Signal #2 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 2-nd CAN output signal
404261	4260	0x10A4	1	Signal #2 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 2-nd CAN output signal
404262	4261	0x10A5	1	Signal #2 Size	Byte	1	0…32	bit	R/W	Size of the 2-nd CAN output signal
404263	4262	0x10A6	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
404265	4264	0x10A8	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
404267	4266	0x10AA	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 3-rd CAN output signal

404268	4267	0x10AB	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
404269	4268	0x10AC	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
404270	4269	0x10AD	1	Signal #3 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 3-rd CAN output signal
404271	4270	0x10AE	1	Signal #3 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 3-rd CAN output signal
404272	4271	0x10AF	1	Signal #3 Size	Byte	1	0…32	bit	R/W	Size of the 3-rd CAN output signal
404273	4272	0x10B0	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
404275	4274	0x10B2	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
404277	4276	0x10B4	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404278	4277	0x10B5	1	Signal #4 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404279	4278	0x10B6	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
404280	4279	0x10B7	1	Signal #4 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404281	4280	0x10B8	1	Signal #4 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404282	4281	0x10B9	1	Signal #4 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404283	4282	0x10BA	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404285	4284	0x10BC	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404287	4286	0x10BE	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404288	4287	0x10BF	1	Signal #5 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404289	4288	0x10C0	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
404290	4289	0x10C1	1	Signal #5 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal

404291	4290	0x10C2	1	Signal #5 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404292	4291	0x10C3	1	Signal #5 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404293	4292	0x10C4	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404295	4294	0x10C6	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404297	4296	0x10C8	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404298	4297	0x10C9	1	Signal #6 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404299	4298	0x10CA	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
404300	4299	0x10CB	1	Signal #6 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404301	4300	0x10CC	1	Signal #6 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404302	4301	0x10CD	1	Signal #6 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404303	4302	0x10CE	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404305	4304	0x10D0	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404307	4306	0x10D2	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404308	4307	0x10D3	1	Signal #7 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404309	4308	0x10D4	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
404310	4309	0x10D5	1	Signal #7 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404311	4310	0x10D6	1	Signal #7 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404312	4311	0x10D7	1	Signal #7 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404313	4312	0x10D8	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404315	4314	0x10DA	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal

404317	4316	0x10DC	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404318	4317	0x10DD	1	Signal #8 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404319	4318	0x10DE	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
404320	4319	0x10DF	1	Signal #8 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404321	4320	0x10E0	1	Signal #8 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404322	4321	0x10E1	1	Signal #8 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404323	4322	0x10E2	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404325	4324	0x10E4	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404327	4326	0x10E6	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404328	4327	0x10E7	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404329	4328	0x10E8	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
404330	4329	0x10E9	1	Signal #9 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404331	4330	0x10EA	1	Signal #9 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404332	4331	0x10EB	1	Signal #9 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404333	4332	0x10EC	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404335	4334	0x10EE	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404337	4336	0x10F0	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404338	4337	0x10F1	1	Signal #10 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal

404339	4338	0x10F2	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
404340	4339	0x10F3	1	Signal #10 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404341	4340	0x10F4	1	Signal #10 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404342	4341	0x10F5	1	Signal #10 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404343	4342	0x10F6	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404345	4344	0x10F8	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404347	4346	0x10FA	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
CAN Transmit 3										
404351	4350	0x10FE	1	CAN Interface	Byte	1 - CAN 1	0 - Undefined, 1 - CAN 1, 2 - CAN 2, 3 - Both Interfaces	N/A	R/W	Defines the CAN interface used to send messages (CAN 1, CAN 2 or Both)
404352	4351	0x10FF	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	CAN message PGN
404354	4353	0x1101	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Transmission Enable. Enables the CAN output message transmission
404355	4354	0x1102	1	Transmission Rate	Word	0	0…10000	ms	R/W	CAN output message transmission rate. If 0 – transmission is upon request.
404356	4355	0x1103	1	Destination Address	Byte	255	0…255	N/A	R/W	Destination address of the PDU1 PGN messages
404357	4356	0x1104	1	Length	Byte	8	0…8	byte	R/W	CAN message data frame length
404358	4357	0x1105	1	Priority	Byte	6	0…7	N/A	R/W	CAN message priority
404359	4358	0x1106	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 1-st CAN output signal
404360	4359	0x1107	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
404361	4360	0x1108	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	

404362	4361	0x1109	1	Signal #1 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 1-st CAN output signal
404363	4362	0x110A	1	Signal #1 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 1-st CAN output signal
404364	4363	0x110B	1	Signal #1 Size	Byte	1	0…32	bit	R/W	Size of the 1-st CAN output signal
404365	4364	0x110C	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
404367	4366	0x110E	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
404369	4368	0x1110	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 2-nd CAN output signal
404370	4369	0x1111	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
404371	4370	0x1112	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
404372	4371	0x1113	1	Signal #2 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 2-nd CAN output signal
404373	4372	0x1114	1	Signal #2 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 2-nd CAN output signal
404374	4373	0x1115	1	Signal #2 Size	Byte	1	0…32	bit	R/W	Size of the 2-nd CAN output signal
404375	4374	0x1116	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
404377	4376	0x1118	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
404379	4378	0x111A	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 3-rd CAN output signal
404380	4379	0x111B	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
404381	4380	0x111C	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
404382	4381	0x111D	1	Signal #3 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 3-rd CAN output signal
404383	4382	0x111E	1	Signal #3 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 3-rd CAN output signal
404384	4383	0x111F	1	Signal #3 Size	Byte	1	0…32	bit	R/W	Size of the 3-rd CAN output signal
404385	4384	0x1120	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal

404387	4386	0x1122	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
404389	4388	0x1124	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404390	4389	0x1125	1	Signal #4 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404391	4390	0x1126	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
404392	4391	0x1127	1	Signal #4 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404393	4392	0x1128	1	Signal #4 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404394	4393	0x1129	1	Signal #4 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404395	4394	0x112A	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404397	4396	0x112C	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404399	4398	0x112E	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404400	4399	0x112F	1	Signal #5 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404401	4400	0x1130	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
404402	4401	0x1131	1	Signal #5 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404403	4402	0x1132	1	Signal #5 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404404	4403	0x1133	1	Signal #5 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404405	4404	0x1134	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404407	4406	0x1136	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404409	4408	0x1138	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal

404410	4409	0x1139	1	Signal #6 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404411	4410	0x113A	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
404412	4411	0x113B	1	Signal #6 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404413	4412	0x113C	1	Signal #6 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404414	4413	0x113D	1	Signal #6 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404415	4414	0x113E	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404417	4416	0x1140	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404419	4418	0x1142	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404420	4419	0x1143	1	Signal #7 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404421	4420	0x1144	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
404422	4421	0x1145	1	Signal #7 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404423	4422	0x1146	1	Signal #7 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404424	4423	0x1147	1	Signal #7 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404425	4424	0x1148	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404427	4426	0x114A	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404429	4428	0x114C	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404430	4429	0x114D	1	Signal #8 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404431	4430	0x114E	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
404432	4431	0x114F	1	Signal #8 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal

44033	4432	0x1150	1	Signal #8 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
44034	4433	0x1151	1	Signal #8 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
44035	4434	0x1152	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
44037	4436	0x1154	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
44039	4438	0x1156	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
44040	4439	0x1157	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
44041	4440	0x1158	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
44042	4441	0x1159	1	Signal #9 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
44043	4442	0x115A	1	Signal #9 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
44044	4443	0x115B	1	Signal #9 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
44045	4444	0x115C	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
44047	4446	0x115E	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
44049	4448	0x1160	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
44050	4449	0x1161	1	Signal #10 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
44051	4450	0x1162	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
44052	4451	0x1163	1	Signal #10 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
44053	4452	0x1164	1	Signal #10 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
44054	4453	0x1165	1	Signal #10 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
44055	4454	0x1166	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
44057	4456	0x1168	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal

40459	4458	0x116A	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
CAN Transmit 4										
40463	4462	0x116E	1	CAN Interface	Byte	1 - CAN 1	0 - Undefined, 1 - CAN 1, 2 - CAN 2, 3 - Both Interfaces	N/A	R/W	Defines the CAN interface used to send messages (CAN 1, CAN 2 or Both)
40464	4463	0x116F	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	CAN message PGN
40466	4465	0x1171	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Transmission Enable. Enables the CAN output message transmission
40467	4466	0x1172	1	Transmission Rate	Word	0	0…10000	ms	R/W	CAN output message transmission rate. If 0 – transmission is upon request.
40468	4467	0x1173	1	Destination Address	Byte	255	0…255	N/A	R/W	Destination address of the PDU1 PGN messages
40469	4468	0x1174	1	Length	Byte	8	0…8	byte	R/W	CAN message data frame length
40470	4469	0x1175	1	Priority	Byte	6	0…7	N/A	R/W	CAN message priority
40471	4470	0x1176	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 1-st CAN output signal
40472	4471	0x1177	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
40473	4472	0x1178	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
40474	4473	0x1179	1	Signal #1 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 1-st CAN output signal
40475	4474	0x117A	1	Signal #1 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 1-st CAN output signal
40476	4475	0x117B	1	Signal #1 Size	Byte	1	0…32	bit	R/W	Size of the 1-st CAN output signal
40477	4476	0x117C	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
40479	4478	0x117E	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal

404481	4480	0x1180	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 2-nd CAN output signal
404482	4481	0x1181	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
404483	4482	0x1182	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
404484	4483	0x1183	1	Signal #2 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 2-nd CAN output signal
404485	4484	0x1184	1	Signal #2 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 2-nd CAN output signal
404486	4485	0x1185	1	Signal #2 Size	Byte	1	0…32	bit	R/W	Size of the 2-nd CAN output signal
404487	4486	0x1186	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
404489	4488	0x1188	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
404491	4490	0x118A	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 3-rd CAN output signal
404492	4491	0x118B	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
404493	4492	0x118C	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
404494	4493	0x118D	1	Signal #3 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 3-rd CAN output signal
404495	4494	0x118E	1	Signal #3 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 3-rd CAN output signal
404496	4495	0x118F	1	Signal #3 Size	Byte	1	0…32	bit	R/W	Size of the 3-rd CAN output signal
404497	4496	0x1190	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
404499	4498	0x1192	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
404501	4500	0x1194	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404502	4501	0x1195	1	Signal #4 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal

404503	4502	0x1196	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
404504	4503	0x1197	1	Signal #4 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404505	4504	0x1198	1	Signal #4 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404506	4505	0x1199	1	Signal #4 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404507	4506	0x119A	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404509	4508	0x119C	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404511	4510	0x119E	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404512	4511	0x119F	1	Signal #5 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404513	4512	0x11A0	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
404514	4513	0x11A1	1	Signal #5 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404515	4514	0x11A2	1	Signal #5 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404516	4515	0x11A3	1	Signal #5 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404517	4516	0x11A4	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404519	4518	0x11A6	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404521	4520	0x11A8	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404522	4521	0x11A9	1	Signal #6 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404523	4522	0x11AA	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
404524	4523	0x11AB	1	Signal #6 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404525	4524	0x11AC	1	Signal #6 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404526	4525	0x11AD	1	Signal #6 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal

40452	4526	0x11AE	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
40454	4528	0x11B0	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
40456	4530	0x11B2	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
40457	4531	0x11B3	1	Signal #7 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
40458	4532	0x11B4	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
40459	4533	0x11B5	1	Signal #7 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
40460	4534	0x11B6	1	Signal #7 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
40461	4535	0x11B7	1	Signal #7 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
40462	4536	0x11B8	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
40464	4538	0x11BA	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
40466	4540	0x11BC	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
40467	4541	0x11BD	1	Signal #8 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
40468	4542	0x11BE	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
40469	4543	0x11BF	1	Signal #8 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
40470	4544	0x11C0	1	Signal #8 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
40471	4545	0x11C1	1	Signal #8 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
40472	4546	0x11C2	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
40474	4548	0x11C4	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
40476	4550	0x11C6	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete,	N/A	R/W	Type of the 4-th CAN output signal

							2 - Continuous			
404552	4551	0x11C7	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404553	4552	0x11C8	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
404554	4553	0x11C9	1	Signal #9 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404555	4554	0x11CA	1	Signal #9 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404556	4555	0x11CB	1	Signal #9 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404557	4556	0x11CC	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404559	4558	0x11CE	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404561	4560	0x11D0	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404562	4561	0x11D1	1	Signal #10 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404563	4562	0x11D2	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
404564	4563	0x11D3	1	Signal #10 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404565	4564	0x11D4	1	Signal #10 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404566	4565	0x11D5	1	Signal #10 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404567	4566	0x11D6	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404569	4568	0x11D8	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404571	4570	0x11DA	4	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.
CAN Transmit 5										

40475	4574	0x11DE	1	CAN Interface	Byte	1 - CAN 1	0 - Undefined, 1 - CAN 1, 2 - CAN 2, 3 - Both Interfaces	N/A	R/W	Defines the CAN interface used to send messages (CAN 1, CAN 2 or Both)
40476	4575	0x11DF	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	CAN message PGN
40478	4577	0x11E1	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Transmission Enable. Enables the CAN output message transmission
40479	4578	0x11E2	1	Transmission Rate	Word	0	0…10000	ms	R/W	CAN output message transmission rate. If 0 – transmission is upon request.
40480	4579	0x11E3	1	Destination Address	Byte	255	0…255	N/A	R/W	Destination address of the PDU1 PGN messages
40481	4580	0x11E4	1	Length	Byte	8	0…8	byte	R/W	CAN message data frame length
40482	4581	0x11E5	1	Priority	Byte	6	0…7	N/A	R/W	CAN message priority
40483	4582	0x11E6	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 1-st CAN output signal
40484	4583	0x11E7	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
40485	4584	0x11E8	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
40486	4585	0x11E9	1	Signal #1 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 1-st CAN output signal
40487	4586	0x11EA	1	Signal #1 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 1-st CAN output signal
40488	4587	0x11EB	1	Signal #1 Size	Byte	1	0…32	bit	R/W	Size of the 1-st CAN output signal
40489	4588	0x11EC	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
40491	4590	0x11EE	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
40493	4592	0x11F0	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 2-nd CAN output signal
40494	4593	0x11F1	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal

404595	4594	0x11F2	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
404596	4595	0x11F3	1	Signal #2 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 2-nd CAN output signal
404597	4596	0x11F4	1	Signal #2 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 2-nd CAN output signal
404598	4597	0x11F5	1	Signal #2 Size	Byte	1	0…32	bit	R/W	Size of the 2-nd CAN output signal
404599	4598	0x11F6	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
404601	4600	0x11F8	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
404603	4602	0x11FA	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 3-rd CAN output signal
404604	4603	0x11FB	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
404605	4604	0x11FC	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
404606	4605	0x11FD	1	Signal #3 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 3-rd CAN output signal
404607	4606	0x11FE	1	Signal #3 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 3-rd CAN output signal
404608	4607	0x11FF	1	Signal #3 Size	Byte	1	0…32	bit	R/W	Size of the 3-rd CAN output signal
404609	4608	0x1200	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
404611	4610	0x1202	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
404613	4612	0x1204	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404614	4613	0x1205	1	Signal #4 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404615	4614	0x1206	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
404616	4615	0x1207	1	Signal #4 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404617	4616	0x1208	1	Signal #4 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404618	4617	0x1209	1	Signal #4 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal

404619	4618	0x120A	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404621	4620	0x120C	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404623	4622	0x120E	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404624	4623	0x120F	1	Signal #5 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404625	4624	0x1210	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
404626	4625	0x1211	1	Signal #5 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404627	4626	0x1212	1	Signal #5 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404628	4627	0x1213	1	Signal #5 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404629	4628	0x1214	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404631	4630	0x1216	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404633	4632	0x1218	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404634	4633	0x1219	1	Signal #6 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404635	4634	0x121A	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
404636	4635	0x121B	1	Signal #6 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404637	4636	0x121C	1	Signal #6 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404638	4637	0x121D	1	Signal #6 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404639	4638	0x121E	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404641	4640	0x1220	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404643	4642	0x1222	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete,	N/A	R/W	Type of the 4-th CAN output signal

							2 - Continuous			
40644	4643	0x1223	1	Signal #7 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
40645	4644	0x1224	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
40646	4645	0x1225	1	Signal #7 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
40647	4646	0x1226	1	Signal #7 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
40648	4647	0x1227	1	Signal #7 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
40649	4648	0x1228	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
40651	4650	0x122A	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
40653	4652	0x122C	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
40654	4653	0x122D	1	Signal #8 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
40655	4654	0x122E	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
40656	4655	0x122F	1	Signal #8 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
40657	4656	0x1230	1	Signal #8 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
40658	4657	0x1231	1	Signal #8 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
40659	4658	0x1232	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
40661	4660	0x1234	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
40663	4662	0x1236	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
40664	4663	0x1237	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
40665	4664	0x1238	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	

404666	4665	0x1239	1	Signal #9 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404667	4666	0x123A	1	Signal #9 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404668	4667	0x123B	1	Signal #9 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404669	4668	0x123C	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404671	4670	0x123E	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404673	4672	0x1240	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined, 1 - Discrete, 2 - Continuous	N/A	R/W	Type of the 4-th CAN output signal
404674	4673	0x1241	1	Signal #10 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
404675	4674	0x1242	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
404676	4675	0x1243	1	Signal #10 Byte Position	Byte	1	0…8	N/A	R/W	Byte position of the 4-th CAN output signal
404677	4676	0x1244	1	Signal #10 Bit Position	Byte	1	0…8	N/A	R/W	Bit position of the 4-th CAN output signal
404678	4677	0x1245	1	Signal #10 Size	Byte	1	0…32	bit	R/W	Size of the 4-th CAN output signal
404679	4678	0x1246	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
404681	4680	0x1248	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
404683	4682	0x124A	#REF!	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing is allowed but does not change the value.